

e-STUDIO400AC Series

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acl, adduser, agg, alsa-lib, alsa-utils, apt, at, attr, audit, base-files, base-passwd, bash, bc, binutils, Buildroot, BusyBox, bzip2, ca-certificates, ccs-tools, chkconfig, cluster-glue, console-setup, coreboot, cpio, cracklib, cron, cryptsetup, cups, CyrusSASL, dbus, dbus-glib, dcfldd, debianutils, dibbler, dmapi, dpkg, e2fsprogs, ElectricFence, elfutils, encryptionSetup, ethtool, fam, fbshot, flashrom, flex, fonts-dejavu, FreeType, ftplib, ftplib++, ftpserver, fuse, gawk, gcc, gdb, gdbm, gettext, glew, glib2, glibc, gmp, gnutls, grub, gstreamer, hdparm, heartbeat, hostname,	Exhibit 2

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<p>htop, hwdata, ifupdown, inserv, iproute, IPRTTools, ipsec-tools, iptables, iputils, kbd, kerberos-config, kexec-tools, keyutils, kmod, krb5, libaio, libbsd, libcap, libcap-ng, libdrm, libev, libevdev, libftdi, libgcrypt, libglu, libgpg-error, libidn, libjpeg, libpng, libprelude, libsamplerate, libselinux, libtasn1, libtirpc, libtool, libtheora, libusb, libxml diff, libxslt, linux-firmware, Linux kernel, lm-sensors, logrotate, lrzsz, lsb, lsof, ltrace, lttng-tools, lttng-ust, LVM2, LZO, lzop, make-dfsg, makedevs, mhash, mingetty, minicom, mpclib, multipath-tools, ncpfs, neon, netatalk, netbase, nettle, net-tools, nfs-utils, Nimbus Sans L font, ntp, openDHCP Server, OpenIPMI, oprofile, Linux-PAM, parted, paxctl, pciutils, perl, policycoreutils, popt, prelink, procps, psmisc, readline, rng-tools, rtk8822BTdriver, rtl8822BU Wi-Fi driver, samhain, sed, sensible-utils, setools, SFML, shadow, slang, smartmontools, strace, sysfsutils, sysklogd, syslog-ng, sysstat, systemd, sysvinit, sysvinit-inittab, tar, tcl, tcpdump, time, trace-cmd, traceroute, tree, uClibc, udev-extraconf, unifont, update-inetd, update-rc.d, usbutils, userspace-rcu, util-linux, V8 JavaScript Engine, valgrind, vim, vsftpd, watchdog, wget, whichman, wireless-tools, x11vnc, x11-xkb-utils, xapian-core, xfsdump, xfsprogs, xkeyboard-config, xz-utils</p>	
<p>at, autotools-dev, bash, binutils, bison, Boost, Buildroot, coreutils, corosync, cpio, CyrusSASL, diffutils, ed, elfutils, findutils, flex, fontconfig, gawk, gcc, gdb, gettext, glibc, gmp, gnutils, grep, grub, gstreamer, gzip, httpd, ifenslave, inetutils, kbd, less, libcap-ng, libffi, libgcrypt, libidn, libprelude, libtasn1, linux-firmware, Linux-PAM, lttng-tools, lttng-ust, m4, make-dfsg, mesa, ntp, parted, perl, procps, readline, sed, SFML, tar, userspace-rcu, util-linux, wget, xkeyboard-config, xorg-server, xz-utils</p>	Exhibit 3
<p>binutils, cups, e2fsprogs, flex, ftplib, fuse, gawk, gcc, gdb, gettext, glib2, gnutils, gstreamer, ImageMagick, libftdi, libgcrypt,</p>	Exhibit 4

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libglu, libgpg-error, libprelude, libsdl, libtheora, lsof, make-dfsg, mhash, minicom, multipath-tools, nettle, neon, newt, OpenIPMI, Linux-PAM, prelink, procps, samhain, SFML, systemd, uClibc, valgrind	
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```

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<signature of Ty Coon>, 1 April 1989
Ty Coon, President of Vice

That's all there is to it!

[Exhibit-2]

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```
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```

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```
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[Exhibit-6]

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Version 3, 29 June 2007

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Beware that some of this code is subtly aware of the way operator precedence is structured in regular expressions. Serious changes in regular-expression syntax might require a total rethink.

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Then the package has been maintained by Charles Briscoe-Smith <cpbs@debian.org>. I gathered data for the 1999-2001 calendar files from various sources on the Internet, and I'd also like to thank Oliver Elphick, Julian Gilbey, Daniel Martin and Jaldhar H. Vyas for providing much useful data on the various religious calendars. I have edited the files they provided to fit calendar's requirements, so any errors should be attributed to me.

After cpbs@debian.org, Marco d'Itri <md@linux.it> maintained it for almost two years, before Tollef Fog Heen <tfheen@debian.org> took over. The package is now maintained by Graham Wilson <bob@decoy.wox.org>.

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Julian Seward, jseward@bzip.org
bzip2/libbzip2 version 1.0.6 of 6 September 2010

[Exhibit-23]

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Source: <http://ftp.debian.org/debian/pool/main/c/ca-certificates/>

Files: debian/*

examples/*

Makefile

mozilla/*

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/usr/src/gnu/lib/libdialog/dir.c

Program: dir.c
Author: Marc van Kempen
desc: Directory routines, sorting and reading

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75dpi-courB12-IL2.bdf 75dpi-courB14.bdf 75dpi-courB14c.bdf
75dpi-courB14-IL2.bdf 75dpi-courR12.bdf 75dpi-courR12c.bdf
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100dpi-lutBS08-IL2.bdf 100dpi-lutBS12-IL2.bdf
100dpi-lutBS14-IL2.bdf 100dpi-lutBS14.bdf 100dpi-lutBS19-IL2.bdf
100dpi-lutRS10.bdf 100dpi-lutRS10-IL2.bdf 100dpi-lutRS08.bdf
100dpi-lutRS08-IL2.bdf 100dpi-lutRS14-IL2.bdf 100dpi-lutRS14.bdf
100dpi-lutRS19-IL2.bdf 75dpi-lutBS12.bdf 75dpi-lutBS12c.bdf
75dpi-lutBS12-IL2.bdf 75dpi-lutBS14.bdf 75dpi-lutBS14c.bdf
75dpi-lutBS14-IL2.bdf 75dpi-lutRS12.bdf 75dpi-lutRS12c.bdf
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legacy12c.bdf, legacy12d.bdf, legacy14a.bdf, legacy14b.bdf,
legacy14c.bdf, legacy14d.bdf, legacy14e.bdf, legacy14f.bdf,
legacy14g.bdf, legacy14h.bdf, legacy14i.bdf, legacy14j.bdf,
legacy14k.bdf, legacy14l.bdf, legacy16a.bdf, legacy16b.bdf,
legacy16c.bdf, legacy16d.bdf, legacy16e.bdf, legacy16f.bdf,
legacy16g.bdf, legacy16h.bdf, legacy16i.bdf, legacy16j.bdf,
legacy16k.bdf, legacy16l.bdf, legacy16m.bdf, legacy19a.bdf,
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#####*/
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The data format used by the zlib library is described by RFCs (Request for Comments) 1950 to 1952 in the files ftp://ds.internic.net/rfc/rfc1950.txt (zlib format), rfc1951.txt (deflate format) and rfc1952.txt (gzip format).

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Mesa 3-D graphics library
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Language: uk

Territory: UA

Revision: 2.1.12

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*/

```

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```

/*****
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* National Center for Supercomputing Applications
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* 605 E. Springfield, Champaign, IL 61820
* httpd@ncsa.uiuc.edu
*
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```
*/
```

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```
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*/
```

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```
*/
/*
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```

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Written by Adam Twiss (adam@zeus.co.uk). March 1996

Thanks to the following people for their input:

Mike Belshe (mbelshe@netscape.com)

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Originally written by Tony Finch <fanf@demon.net> <dot@dotat.at>.

Implementation ideas were taken from mod_alias.c. The overall concept is derived from the OVERRIDE_DOC_ROOT/OVERRIDE_CGIDIR patch to Apache 1.3b3 and a similar feature in Demon's thttpd, both written by James Grinter <jrg@blodwen.demon.co.uk>.

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automatic stack-based locking object

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"src/lib/gssapi", including the following files:

```
lib/gssapi/generic/gssapi_err_generic.et  
lib/gssapi/mechglue/g_accept_sec_context.c  
lib/gssapi/mechglue/g_acquire_cred.c  
lib/gssapi/mechglue/g_canon_name.c  
lib/gssapi/mechglue/g_compare_name.c  
lib/gssapi/mechglue/g_context_time.c  
lib/gssapi/mechglue/g_delete_sec_context.c  
lib/gssapi/mechglue/g_dsp_name.c  
lib/gssapi/mechglue/g_dsp_status.c  
lib/gssapi/mechglue/g_dup_name.c  
lib/gssapi/mechglue/g_exp_sec_context.c  
lib/gssapi/mechglue/g_export_name.c  
lib/gssapi/mechglue/g_glue.c  
lib/gssapi/mechglue/g_imp_name.c  
lib/gssapi/mechglue/g_imp_sec_context.c  
lib/gssapi/mechglue/g_init_sec_context.c  
lib/gssapi/mechglue/g_initialize.c  
lib/gssapi/mechglue/g_inquire_context.c  
lib/gssapi/mechglue/g_inquire_cred.c  
lib/gssapi/mechglue/g_inquire_names.c  
lib/gssapi/mechglue/g_process_context.c  
lib/gssapi/mechglue/g_rel_buffer.c  
lib/gssapi/mechglue/g_rel_cred.c  
lib/gssapi/mechglue/g_rel_name.c  
lib/gssapi/mechglue/g_rel_oid_set.c
```

lib/gssapi/mechglue/g_seal.c
lib/gssapi/mechglue/g_sign.c
lib/gssapi/mechglue/g_store_cred.c
lib/gssapi/mechglue/g_unseal.c
lib/gssapi/mechglue/g_userok.c
lib/gssapi/mechglue/g_utils.c
lib/gssapi/mechglue/g_verify.c
lib/gssapi/mechglue/gssd_pname_to_uid.c
lib/gssapi/mechglue/mglueP.h
lib/gssapi/mechglue/oid_ops.c
lib/gssapi/spnego/gssapiP_spnego.h
lib/gssapi/spnego/spnego_mech.c

and the initial implementation of incremental propagation, including the following new or changed files:

include/ipropr_hdr.h
kadmin/server/ipropr_svc.c
lib/kdb/ipropr.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
lib/kdb/kdb_log.h
lib/krb5/error_tables/kdb5_err.et
slave/kpropd_rpc.c
slave/kproplog.c

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This code is derived from section 17.1 of Applied Cryptography, second edition, which describes a stream cipher allegedly compatible with RSA Labs "RC4" cipher (the actual description of which is a trade secret). The same algorithm is used as a stream cipher called "arcfour" in Tatu Ylonen's ssh package.

Here the stream cipher has been modified always to include the time when initializing the state. That makes it impossible to regenerate the same random sequence twice, so this can't be used for encryption, but will generate good random numbers.

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This code implements the MD5 message-digest algorithm. The algorithm is due to Ron Rivest. This code was written by Colin Plumb in 1993, no copyright is claimed. This code is in the public domain; do with it what you wish.

Equivalent code is available from RSA Data Security, Inc.
This code has been tested against that, and is equivalent,
except that you don't need to include two pages of legalese
with every copy.

To compute the message digest of a chunk of bytes, declare an
MD5Context structure, pass it to MD5Init, call MD5Update as
needed on buffers full of bytes, and then call MD5Final, which
will fill a supplied 16-byte array with the digest.

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for ron@debian.org; Sun, 14 Aug 2011 05:47:35 +0000
In-Reply-To: <20110813065417.GV2324@audi.shelbyville.oz>
References: <20110813065417.GV2324@audi.shelbyville.oz>
From: Silvia Pfeiffer <silviapfeiffer1@gmail.com>
Date: Sun, 14 Aug 2011 15:47:09 +1000
Message-ID: <CAHp8n2nmGN+G09aA118KN86NeX9yw_VJAQNFZebm74k195zz+Q@mail.gmail.com>
Subject: Re: Distributing the libogg RFCs in the Debian package
To: Ron <ron@debian.org>
Cc: Christopher Montgomery <monty@xiph.org>,
Ivo Emanuel Goncalves <justivo@gmail.com>

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```
printf("%s",png_get_copyright(NULL));
```

Also, the PNG logo (in PNG format, of course) is supplied in the
files "pngbar.png" and "pngbar.jpg (88x31) and "pngnow.png" (98x31).

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Glenn Randers-Pehrson

glennrp at users.sourceforge.net
July 10, 2012

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Version 2 RFC1832: XDR: External Data REpresentation
Standard RFC1833: Binding Protocols for ONC RPC Version 2
RFC2078: Generic Security Service Application Program
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Berlin, 28.11.1994
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* WHENCE *

This file attempts to document the origin and licensing information, if known, for each piece of firmware distributed for use with the Linux kernel.

Driver: ambassador -- Madge Ambassador (Collage PCI 155 Server) ATM NIC.

File: atmsar11.fw

Licence: Allegedly GPLv2+, but no source visible. Marked:

Madge Ambassador ATM Adapter microcode.
Copyright (C) 1995-1999 Madge Networks Ltd.

This microcode data is placed under the terms of the GNU General Public License. The GPL is contained in /usr/doc/copyright/GPL on a Debian system and in the file COPYING in the Linux kernel source.

We would prefer you not to distribute modified versions without consultation and not to ask for assembly/other microcode source.

Driver: snd-korg1212 -- Korg 1212 IO audio device

File: korg/k1212.dsp

Licence: Unknown

Found in alsa-firmware package in hex form; no licensing information.

Driver: snd-maestro3 -- ESS Allegro Maestro3 audio device

File: ess/maestro3_assp_kernel.fw

File: ess/maestro3_assp_minisrc.fw

Licence: Unknown

Found in alsa-firmware package in hex form with a comment claiming to be GPLv2+, but without source -- and with another comment saying "ESS drops binary dsp code images on our heads, but we don't get to see specs on the dsp."

Driver: snd-ymfpci -- Yamaha YMF724/740/744/754 audio devices

File: yamaha/ds1_ctrl.fw
File: yamaha/ds1_dsp.fw
File: yamaha/ds1e_ctrl.fw

Licence: Unknown

Found alsa-firmware package in hex form, with the following comment:
Copyright (c) 1997-1999 Yamaha Corporation. All Rights Reserved.

Driver: advansys - AdvanSys SCSI

File: advansys/mcode.bin
File: advansys/3550.bin
File: advansys/38C0800.bin
File: advansys/38C1600.bin

Licence: BSD, no source available.

Found in hex form in kernel source.

Driver: qla1280 - Qlogic QLA 1240/1x80/1x160 SCSI support

File: qllogic/1040.bin
File: qllogic/1280.bin
File: qllogic/12160.bin

Licence: Redistributable. See LICENCE.qla1280 for details

Driver: smctr -- SMC ISA/MCA Token Ring adapter

File: tr_smctr.bin
Version: 6.3C1
Info: MCT.BIN v6.3C1 03/01/95

Original licence info:

* This firmware is licensed to you strictly for use in conjunction
* with the use of SMC TokenRing adapters. There is no warranty
* expressed or implied about its fitness for any purpose.

Driver: kaweth -- USB KLSI KL5USB101-based Ethernet device

File: kaweth/new_code.bin
File: kaweth/new_code_fix.bin
File: kaweth/trigger_code.bin
File: kaweth/trigger_code_fix.bin

Licence: Redistributable. See LICENCE.kaweth for details

Found in hex form in the kernel source.

Driver: dvb-ttusb-budget -- Technotrend/Hauppauge Nova-USB devices

File: ttusb-budget/dspbootcode.bin

Licence: Unknown

Found in hex form in the kernel source.

Driver: keyspan -- USB Keyspan USA-xxx serial device

File: keyspan/mpr.fw
File: keyspan/usa18x.fw
File: keyspan/usa19.fw
File: keyspan/usa19qi.fw
File: keyspan/usa19qw.fw
File: keyspan/usa19w.fw
File: keyspan/usa28.fw
File: keyspan/usa28xa.fw
File: keyspan/usa28xb.fw
File: keyspan/usa28x.fw
File: keyspan/usa49w.fw
File: keyspan/usa49wlc.fw

Converted from Intel HEX files, used in our binary representation of ihex.

Original licence information:

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Driver: keyspan_pda -- USB Keyspan PDA single-port serial device

File: keyspan_pda/keyspan_pda.fw
Source: keyspan_pda/keyspan_pda.S

File: keyspan_pda/xircom_pgs.fw
Source: keyspan_pda/xircom_pgs.S

Source: keyspan_pda/Makefile

Licence: GPLv2 or later. See GPL-2 and GPL-3 for details.

Compiled from original 8051 source into Intel HEX, used in our binary ihex form.

Driver: emi26 -- EMI 2|6 USB Audio interface

File: emi26/bitstream.fw
Version: 1.1.1.131
Info: DATE=2001dec06

File: emi26/firmware.fw
Version: 1.0.2.916
Info: DATE=12.02.2002

File: emi26/loader.fw

Converted from Intel HEX files, used in our binary representation of ihex.

Original licence information:

```
/*
 * This firmware is for the Emagic EMI 2|6 Audio Interface
 *
 * The firmware contained herein is Copyright (c) 1999-2002 Emagic
 * as an unpublished work. This notice does not imply unrestricted
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 *
 * Permission is hereby granted for the distribution of this firmware
 * image as part of a Linux or other Open Source operating system kernel
 * in text or binary form as required.
 *
 * This firmware may not be modified and may only be used with the
 * Emagic EMI 2|6 Audio Interface. Distribution and/or Modification of
 * any driver which includes this firmware, in whole or in part,
 * requires the inclusion of this statement.
 */
```

Driver: emi62 -- EMI 6|2m USB Audio interface

File: emi62/bitstream.fw
Version: 1.0.0.191
Info: DATE= 2002oct28

File: emi62/loader.fw
Version: 1.0.2.002
Info: DATE=10.01.2002

File: emi62/midi.fw
Version: 1.04.062
Info: DATE=16.10.2002

File: emi62/spdif.fw
Version: 1.04.062
Info: DATE=16.10.2002

Converted from Intel HEX files, used in our binary representation of ihex.

Original licence information: None

Driver: ti_usb_3410_5052 -- USB TI 3410/5052 serial device

File: ti_3410.fw
Info: firmware 9/10/04 FW3410_Special_StartWdogOnStartPort

File: ti_5052.fw
Info: firmware 9/18/04

Licence: Allegedly GPLv2+, but no source visible. Marked:
Copyright (C) 2004 Texas Instruments

Found in hex form in kernel source.

Driver: ti_usb_3410_5052 -- Multi-Tech USB cell modems

File: mts_cdma.fw
File: mts_gsm.fw
File: mts_edge.fw

Licence: "all firmware components are redistributable in binary form"
per support@multitech.com
Copyright (C) 2005 Multi-Tech Systems, Inc.

Found in hex form in ftp://ftp.multitech.com/wireless/wireless_linux.zip

Driver: ti_usb_3410_5052 -- Multi-Tech USB fax modems

File: mts_mt9234mu.fw
File: mts_mt9234zba.fw

Licence: Unknown

Driver: whiteheat -- USB ConnectTech WhiteHEAT serial device

File: whiteheat.fw

Version: 4.06

File: whiteheat_loader.fw

Licence: Allegedly GPLv2, but no source visible. Marked:
Copyright (C) 2000-2002 ConnectTech Inc

Debug loader claims the following behaviour:

- Port 1 LED flashes when the vend_ax program is running
- Port 2 LED flashes when any SETUP command arrives
- Port 3 LED flashes when any valid VENDOR request occurs
- Port 4 LED flashes when the EXTERNAL RAM DOWNLOAD request occurs

Converted from Intel HEX files, used in our binary representation of ihex.

Driver: ip2 -- Computone IntelliPort Plus serial device

File: intelliport2.bin

Licence: Unknown

Found in hex form in kernel source.

Driver: cpia2 -- cameras based on Vision's CPiA2

File: cpia2/stv0672_vp4.bin

Licence: Allegedly GPLv2+, but no source visible. Marked:
Copyright (C) 2001 STMicroelectronics, Inc.
Contact: steve.miller@st.com
Description: This file contains patch data for the CPiA2 (stv0672) VP4.

Found in hex form in kernel source.

Driver: dabusb -- Digital Audio Broadcasting (DAB) Receiver for USB and Linux

File: dabusb/firmware.fw

File: dabusb/bitstream.bin

Licence: Distributable

- * Copyright (C) 1999 BayCom GmbH
- *
- * Redistribution and use in source and binary forms, with or without
- * modification, are permitted provided that redistributions of source
- * code retain the above copyright notice and this comment without
- * modification.

Driver: vicam -- USB 3com HomeConnect (aka vicam)

File: vicam/firmware.fw

Licence: Unknown

Found in hex form in kernel source.

Driver: io_edgeport - USB Inside Out Edgeport Serial Driver

File: edgeport/boot.fw
File: edgeport/boot2.fw
File: edgeport/down.fw
File: edgeport/down2.fw

Licence: Allegedly GPLv2+, but no source visible. Marked:

```
*****  
/* Edgeport/4 Binary Image  
/* Generated by HEX2C v1.06  
/* Copyright (C) 1998 Inside Out Networks, All rights reserved.  
*****
```

Found in hex form in kernel source.

Driver: io_ti - USB Inside Out Edgeport Serial Driver
(TI Devices)

File: edgeport/down3.bin

Licence:

```
*****  
/* Edgeport Binary Image (for TI based products)  
/* Generated by TIBin2C v2.00 (watchport)  
/* Copyright (C) 2001 Inside Out Networks, All rights reserved.  
*****
```

Found in hex form in kernel source.

Driver: dsp56k - Atari DSP56k support

File: dsp56k/bootstrap.bin
Source: dsp56k/bootstrap.asm
Source: dsp56k/Makefile
Source: dsp56k/concat-bootstrap.pl

Licence: GPLv2 or later. See GPL-2 and GPL-3 for details.

DSP56001 assembler, buildable with a56 from
<http://www.zdomain.com/a56.html>

Driver: snd-sb16-csp - Sound Blaster 16/AWE CSP support

File: sb16/mulaw_main.csp
File: sb16/alaw_main.csp
File: sb16/ima_adpcm_init.csp
File: sb16/ima_adpcm_playback.csp

File: sb16/ima_adpcm_capture.csp

Licence: Allegedly GPLv2+, but no source visible. Marked:

/*

* Copyright (c) 1994 Creative Technology Ltd.
* Microcode files for SB16 Advanced Signal Processor
*/

Found in hex form in kernel source.

Driver: qla2xxx - QLogic QLA2XXX Fibre Channel

File: ql2100_fw.bin
Version: 1.19.38 TP
File: ql2200_fw.bin
Version: 2.02.08 TP
File: ql2300_fw.bin
Version: 3.03.28 IPX
File: ql2322_fw.bin
Version: 3.03.28 IPX
File: ql2400_fw.bin
Version: 8.06.00 MID
File: ql2500_fw.bin
Version: 8.06.00 MIDQ

Licence: Redistributable. See LICENCE.qla2xxx for details

Available from <http://ldriver.qlogic.com/firmware/>

Driver: orinoco - Agere/Prism/Symbol Orinoco support

File: agere_sta_fw.bin
Version: 9.48 Hermes I
File: agere_ap_fw.bin
Version: 9.48 Hermes I

Licence: Redistributable. See LICENCE.agere for details

Driver: ar9170 - Atheros 802.11n "otus" USB

File: ar9170-1.fw
File: ar9170-2.fw

Licence: Redistributable. See LICENCE.atheros_firmware for details

Driver: ath9k_htc - Atheros HTC devices (USB)

File: ar9271.fw
File: ar7010.fw
File: ar7010_1_1.fw
File: htc_9271.fw
Version: 1.3.1

File: htc_7010.fw
Version: 1.3.1

Licence: Redistributable. See LICENCE.atheros_firmware for details

File: ath9k_htc/htc_7010-1.4.0.fw
Version: 1.4.0
File: ath9k_htc/htc_9271-1.4.0.fw
Version: 1.4.0

Licence: Free software. See LICENCE.open-ath9k-htc-firmware for details

Driver: cassini - Sun Cassini

File: sun/cassini.bin

Licence: Unknown

Found in hex form in kernel source.

Driver: slicoss - Alacritech IS-NIC products

File: slicoss/gbdownload.sys
File: slicoss/gbrcvcode.sys
File: slicoss/oasisdbgdownload.sys
File: slicoss/oasisdownload.sys
File: slicoss/oasisrcvcode.sys

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Found in hex form in kernel source.

Driver: sxg - Alacritech IS-NIC products

File: sxg/saharadownloadB.sys
File: sxg/saharadbdownloadB.sys

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Found in hex form in kernel source.

Driver: cxgb3 - Chelsio Terminator 3 1G/10G Ethernet adapter

File: cxgb3/t3b_psram-1.1.0.bin
File: cxgb3/t3c_psram-1.1.0.bin
File: cxgb3/t3fw-7.0.0.bin
File: cxgb3/t3fw-7.1.0.bin
File: cxgb3/t3fw-7.4.0.bin
File: cxgb3/t3fw-7.10.0.bin
File: cxgb3/t3fw-7.12.0.bin

Licence: GPLv2 or OpenIB.org BSD license, no source visible

Driver: cxgb3 - Chelsio Terminator 3 1G/10G Ethernet adapter

File: cxgb3/ael2005_opt_edc.bin
File: cxgb3/ael2005_twx_edc.bin
File: cxgb3/ael2020_twx_edc.bin

Licence:

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Found in hex form in kernel source.

Driver: cxgb4 - Chelsio Terminator 4/5/6 1/10/25/40/100G Ethernet adapter

File: cxgb4/t4fw-1.14.4.0.bin
File: cxgb4/t4fw-1.15.37.0.bin
File: cxgb4/t4fw-1.16.33.0.bin
Link: cxgb4/t4fw.bin -> t4fw-1.16.33.0.bin
File: cxgb4/t5fw-1.14.4.0.bin
File: cxgb4/t5fw-1.15.37.0.bin
File: cxgb4/t5fw-1.16.33.0.bin
Link: cxgb4/t5fw.bin -> t5fw-1.16.33.0.bin
File: cxgb4/t6fw-1.16.33.0.bin
Link: cxgb4/t6fw.bin -> t6fw-1.16.33.0.bin
File: cxgb4/aq1202_fw.cld
File: cxgb4/bcm8483.bin

Licence: Redistributable. See LICENCE.chelsio_firmware for details

Driver: e100 -- Intel PRO/100 Ethernet NIC

File: e100/d101m_ucose.bin
File: e100/d101s_ucose.bin
File: e100/d102e_ucose.bin

Licence: Redistributable. See LICENCE.e100 for details

Driver: acenic -- Alteon AceNIC Gigabit Ethernet card

File: acenic/tg1.bin
File: acenic/tg2.bin

Licence: Unknown

Found in hex form in kernel source, but source allegedly available at
<http://alteon.shareable.org/>

Driver: tg3 -- Broadcom Tigon3 based gigabit Ethernet cards

File: tigon/tg3.bin
File: tigon/tg3_tso.bin
File: tigon/tg3_tso5.bin
File: tigon/tg357766.bin

Licence:

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- * Copyright (C) 2000-2013 Broadcom Corporation.
- *
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- * data in hexadecimal or equivalent format, provided this copyright
- * notice is accompanying it.

Found in hex form in kernel source.

Driver: starfire - Adaptec Starfire/DuraLAN support

File: adaptec/starfire_rx.bin
File: adaptec/starfire_tx.bin

Licence: Allegedly GPLv2, but no source visible.

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Driver: i2400m-usb - Intel 2400 Wireless WiMAX Connection over USB

File: i2400m-fw-usb-1.4.sbcf
File: i2400m-fw-usb-1.5.sbcf
File: i6050-fw-usb-1.5.sbcf

Licence: Redistributable. See LICENCE.i2400m for details

Also available from <http://linuxwimax.org/Download>

Driver: libertas - Marvell Libertas fullmac-type 802.11b/g cards

File: libertas/cf8381.bin
File: libertas/cf8381_helper.bin
File: libertas/cf8385.bin
File: libertas/cf8385_helper.bin
File: libertas/gspi8682.bin
File: libertas/gspi8682_helper.bin
File: libertas/gspi8686_v9.bin
File: libertas/gspi8686_v9_helper.bin
File: libertas/gspi8688.bin
File: libertas/gspi8688_helper.bin
File: libertas/sd8385.bin
File: libertas/sd8385_helper.bin
File: libertas/sd8682.bin
File: libertas/sd8682_helper.bin
File: libertas/sd8686_v8.bin
File: libertas/sd8686_v8_helper.bin
File: libertas/sd8686_v9.bin
File: libertas/sd8686_v9_helper.bin
File: libertas/usb8388_v5.bin
File: libertas/usb8388_v9.bin
File: libertas/usb8682.bin
File: mrvl/sd8688.bin
Link: libertas/sd8688.bin -> ../mrvl/sd8688.bin
File: mrvl/sd8688_helper.bin
Link: libertas/sd8688_helper.bin -> ../mrvl/sd8688_helper.bin

Licence: Redistributable. See LICENCE.Marvell for details. Extracted from Linux driver tarballs downloaded from Marvell's "Extranet" with permission.

Driver: libertas - Marvell Libertas 802.11b/g cards, OLPC firmware

File: libertas/lbtf_sdio.bin
Version: 9.0.7.p4

File: lbtf_usb.bin
Version: 5.132.3.p1

File: libertas/usb8388_olpc.bin

Version: 5.110.22.p23

Licence: Redistributable. See LICENCE.OLPC for details.

Available from <http://dev.laptop.org/pub/firmware/libertas/>

Driver: mwl8k - Marvell Libertas softmac-type 802.11b/g/n cards

File: mwl8k/fmimage_8687.fw
File: mwl8k/helper_8687.fw
File: mwl8k/fmimage_8366.fw
File: mwl8k/fmimage_8366_ap-1.fw
File: mwl8k/fmimage_8366_ap-2.fw
File: mwl8k/fmimage_8366_ap-3.fw
Version: 5.2.8.16
File: mwl8k/helper_8366.fw

File: mwl8k/fmimage_8764_ap-1.fw
Version: 7.4.0.9

Licence: Redistributable. See LICENCE.Marvell for details. 8687 images
downloaded from Marvell's "Extranet" with permission. 8366 images contributed
directly by Marvell.

Driver: mwifiex - Marvell Wi-Fi fullmac-type 802.11n/ac cards

File: mrvl/sd8787_uapsta.bin
Version: 14.66.35.p52

File: mrvl/usb8766_uapsta.bin
Version: 14.68.22.p16

File: mrvl/sd8797_uapsta.bin
Version: 14.66.11.p151

File: mrvl/usb8797_uapsta.bin
Version: 14.68.29.p49

File: mrvl/sd8897_uapsta.bin
Version: 15.68.7.p87

File: mrvl/usb8897_uapsta.bin
Version: 15.68.4.p103

File: mrvl/pcie8897_uapsta.bin
Version: 15.68.7.p77

File: mrvl/sd8887_uapsta.bin
Version: 15.68.7.p112

File: mrvl/sd8801_uapsta.bin
Version: 14.68.36.p60

File: mrvl/usb8801_uapsta.bin
Version: 14.68.36.p60

File: mrvl/pcieuart8997_combo_v4.bin
Version: 16.68.1.p70

File: mrvl/pcieusb8997_combo_v4.bin
Version: 16.68.1.p97

File: mrvl/pcie8997_wlan_v4.bin
Version: 16.68.1.p97

Licence: Redistributable. See LICENCE.Marvell for details. Originates from
<http://git.marvell.com/?p=mwifwex-firmware.git>

Driver: iwlwifi - Intel Wireless Wifi

File: iwlwifi-3945-2.ucode
Version: 15.32.2.9

File: iwlwifi-4965-2.ucode
Version: 228.61.2.24

File: iwlwifi-5000-1.ucode
Version: 5.4.A.11 (aka 5.4.1.16)

File: iwlwifi-5000-2.ucode
Version: 8.24.2.12

File: iwlwifi-5000-5.ucode
Version: 8.83.5.1

File: iwlwifi-5150-2.ucode
Version: 8.24.2.2

File: iwlwifi-1000-3.ucode
Version: 128.50.3.1

File: iwlwifi-1000-5.ucode
Version: 39.31.5.1

File: iwlwifi-6000-4.ucode
Version: 9.221.4.1

File: iwlwifi-6050-4.ucode
Version: 9.201.4.1

File: iwlwifi-6050-5.ucode
Version: 41.28.5.1

File: iwlwifi-6000g2a-5.ucode
Version: 17.168.5.3

File: iwlwifi-6000g2a-6.ucode
Version: 18.168.6.1

File: iwlwifi-6000g2b-5.ucode
Version: 17.168.5.1

File: iwlwifi-6000g2b-6.ucode

Version: 18.168.6.1

File: iwlwifi-135-6.ucode
Version: 18.168.6.1

File: iwlwifi-100-5.ucode
Version: 39.31.5.1

File: iwlwifi-105-6.ucode
Version: 18.168.6.1

File: iwlwifi-2030-6.ucode
Version: 18.168.6.1

File: iwlwifi-2000-6.ucode
Version: 18.168.6.1

File: iwlwifi-7260-7.ucode
Version: 22.1.7.0

File: iwlwifi-7260-8.ucode
Version: 22.24.8.0

File: iwlwifi-7260-9.ucode
Version: 25.228.9.0

File: iwlwifi-7260-10.ucode
Version: 23.15.10.0

File: iwlwifi-7260-12.ucode
Version: 25.17.12.0

File: iwlwifi-7260-13.ucode
Version: 25.30.13.0

File: iwlwifi-7260-16.ucode
Version 16.242414.0

File: iwlwifi-7260-17.ucode
Version 17.459231.0

File: iwlwifi-3160-7.ucode
Version: 22.1.7.0

File: iwlwifi-3160-8.ucode
Version: 22.24.8.0

File: iwlwifi-3160-9.ucode
Version: 25.228.9.0

File: iwlwifi-3160-10.ucode
Version: 23.15.10.0

File: iwlwifi-3160-12.ucode
Version: 25.17.12.0

File: iwlwifi-3160-13.ucode
Version: 25.30.13.0

File: iwlwifi-3160-16.ucode

Version 16.242414.0

File: iwlwifi-3160-17.ucode
Version 17.459231.0

File: iwlwifi-7265-8.ucode
Version: 22.24.8.0

File: iwlwifi-7265-9.ucode
Version: 25.228.9.0

File: iwlwifi-7265-10.ucode
Version: 23.15.10.0

File: iwlwifi-7265-12.ucode
Version: 25.17.12.0

File: iwlwifi-7265-13.ucode
Version: 25.30.13.0

File: iwlwifi-7265-16.ucode
Version 16.242414.0

File: iwlwifi-7265-17.ucode
Version 17.459231.0

File: iwlwifi-7265D-10.ucode
Version: 23.15.10.0

File: iwlwifi-7265D-12.ucode
Version: 25.17.12.0

File: iwlwifi-7265D-13.ucode
Version: 25.30.13.0

File: iwlwifi-7265D-16.ucode
Version 16.242414.0

File: iwlwifi-7265D-17.ucode
Version 17.352738.0

File: iwlwifi-7265D-21.ucode
Version 21.302800.0

File: iwlwifi-7265D-22.ucode
Version 22.391740.0

File: iwlwifi-7265D-27.ucode
Version 27.455470.0

File: iwlwifi-3168-21.ucode
Version 21.302800.0

File: iwlwifi-3168-22.ucode
Version 22.361476.0

File: iwlwifi-3168-27.ucode
Version 27.455470.0

File: iwlwifi-8000C-13.ucode

Version: 25.30.13.0

File: iwlwifi-8000C-16.ucode
Version 16.242414.0

File: iwlwifi-8000C-21.ucode
Version 21.302800.0

File: iwlwifi-8000C-22.ucode
Version 22.391740.0

File: iwlwifi-8000C-27.ucode
Version 27.455470.0

File: iwlwifi-8265-21.ucode
Version 21.302800.0

File: iwlwifi-8265-22.ucode
Version 22.391740.0

File: iwlwifi-8265-27.ucode
Version 27.455470.0

Licence: Redistributable. See LICENCE.iwlwifi_firmware for details

Also available from <http://wireless.kernel.org/en/users/Drivers/iwlwifi#Firmware>

Driver: cx231xx - Conexant Cx23100/101/102 USB broadcast A/V decoder

File: v4l-cx231xx-avcore-01.fw

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Driver: tehuti - Tehuti Networks 10G Ethernet

File: tehuti/bdx.bin

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Driver: typhoon - 3cr990 series Typhoon

File: 3com/typhoon.bin

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/*

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*/

Found in hex form in kernel source.

Driver: yam - YAM driver for AX.25

File: yam/1200.bin

File: yam/9600.bin

Licence:

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Found in hex form in kernel source.

Driver: 3c359 - 3Com 3C359 Token Link Velocity XL adapter

File: 3com/3C359.bin

Licence:

```
/*
 * The firmware this driver downloads into the tokenring card is a
 * separate program and is not GPL'd source code, even though the Linux
 * side driver and the routine that loads this data into the card are.
 *
 * This firmware is licensed to you strictly for use in conjunction
 * with the use of 3Com 3C359 TokenRing adapters. There is no
 * warranty expressed or implied about its fitness for any purpose.
 */
/* 3c359_microcode.mac: 3Com 3C359 Tokenring microcode.
 *
 * Notes:
 * - Loaded from xl_init upon adapter initialization.
 *
 * Available from 3Com as part of their standard 3C359 driver.
 */
```

Found in hex form in kernel source.

Driver: penet_cs - NE2000 compatible PCMCIA adapter

File: cis/LA-PCM.cis
File: cis/PCMLM28.cis
File: cis/DP83903.cis
File: cis/NE2K.cis
File: cis/tamarack.cis
File: cis/PE-200.cis
File: cis/PE520.cis
Source: cis/

Licence: Dual GPLv2/MPL

Originally developed by the pcmcia-cs project
Copyright (C) 1998, 1999, 2000 David A. Hinds

Driver: 3c589_cs - 3Com PCMCIA adapter

File: cis/3CXEM556.cis
Source: cis/src/3CXEM556.cis

Licence: Dual GPLv2/MPL

Originally developed by the pcmcia-cs project
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Driver: 3c574_cs - 3Com PCMCIA adapter

File: cis/3CCFEM556.cis
Source: cis/src/3CCFEM556.cis

Licence: Dual GPLv2/MPL

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Driver: serial_cs - Serial PCMCIA adapter

File: cis/MT5634ZLX.cis
File: cis/RS-COM-2P.cis
File: cis/COMpad2.cis
File: cis/COMpad4.cis
Source: cis/src/MT5634ZLX.cis
Source: cis/src/RS-COM-2P.cis
Source: cis/src/COMpad2.cis
Source: cis/src/COMpad4.cis

Licence: Dual GPLv2/MPL

Originally developed by the pcmcia-cs project
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Driver: serial_cs - Serial PCMCIA adapter

File: cis/SW_555_SER.cis
File: cis/SW_7xx_SER.cis
File: cis/SW_8xx_SER.cis

Licence: GPLv3. See GPL-3 for details.

Copyright Sierra Wireless

Driver: smc91c92_cs - SMC 91Cxx PCMCIA

File: ositech/Xilinx7OD.bin

Licence: Allegedly GPL, but no source visible. Marked:
This file contains the firmware of Seven of Diamonds from OSITECH.
(Special thanks to Kevin MacPherson of OSITECH)

Found in hex form in kernel source.

Driver: cx23418 - Conexant PCI Broadcast A/V with MPEG encoder

File: v4l-cx23418-apu.fw
File: v4l-cx23418-cpu.fw
File: v4l-cx23418-dig.fw

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Driver: cx23885 - Conexant PCI Express Broadcast A/V decoder

File: v4l-cx23885-avcore-01.fw

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Driver: cx23840 - Conexant sideport Broadcast A/V decoder

File: v4l-cx25840.fw

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Driver: qllogicpti - PTI Qlogic, ISP Driver

File: qllogic/isp1000.bin

Licence: Unknown

Found in hex form in kernel source.

Driver: myri_sbus - MyriCOM Gigabit Ethernet

File: myricom/lanai.bin

Licence: Unknown

Found in hex form in kernel source.

Driver: bnx2x: Broadcom Everest

File: bnx2x-e1-4.8.53.0.fw
File: bnx2x-e1h-4.8.53.0.fw
File: bnx2x-e1-5.2.7.0.fw

File: bnx2x-e1h-5.2.7.0.fw
File: bnx2x-e1-5.2.13.0.fw
File: bnx2x-e1h-5.2.13.0.fw
File: bnx2x/bnx2x-e1-6.0.34.0.fw
File: bnx2x/bnx2x-e1h-6.0.34.0.fw
File: bnx2x/bnx2x-e2-6.0.34.0.fw
File: bnx2x/bnx2x-e1-6.2.5.0.fw
File: bnx2x/bnx2x-e1h-6.2.5.0.fw
File: bnx2x/bnx2x-e2-6.2.5.0.fw
File: bnx2x/bnx2x-e1-6.2.9.0.fw
File: bnx2x/bnx2x-e1h-6.2.9.0.fw
File: bnx2x/bnx2x-e2-6.2.9.0.fw
File: bnx2x/bnx2x-e2-7.0.20.0.fw
File: bnx2x/bnx2x-e1-7.0.20.0.fw
File: bnx2x/bnx2x-e1h-7.0.20.0.fw
File: bnx2x/bnx2x-e2-7.0.23.0.fw
File: bnx2x/bnx2x-e1-7.0.23.0.fw
File: bnx2x/bnx2x-e1h-7.0.23.0.fw
File: bnx2x/bnx2x-e2-7.0.29.0.fw
File: bnx2x/bnx2x-e1-7.0.29.0.fw
File: bnx2x/bnx2x-e1h-7.0.29.0.fw
File: bnx2x/bnx2x-e2-7.2.16.0.fw
File: bnx2x/bnx2x-e1-7.2.16.0.fw
File: bnx2x/bnx2x-e1h-7.2.16.0.fw
File: bnx2x/bnx2x-e2-7.2.51.0.fw
File: bnx2x/bnx2x-e1-7.2.51.0.fw
File: bnx2x/bnx2x-e1h-7.2.51.0.fw
File: bnx2x/bnx2x-e1-7.8.2.0.fw
File: bnx2x/bnx2x-e1h-7.8.2.0.fw
File: bnx2x/bnx2x-e2-7.8.2.0.fw
File: bnx2x/bnx2x-e1-7.8.17.0.fw
File: bnx2x/bnx2x-e1h-7.8.17.0.fw
File: bnx2x/bnx2x-e2-7.8.17.0.fw
File: bnx2x/bnx2x-e1-7.8.19.0.fw
File: bnx2x/bnx2x-e1h-7.8.19.0.fw
File: bnx2x/bnx2x-e2-7.8.19.0.fw
File: bnx2x/bnx2x-e1-7.10.51.0.fw
File: bnx2x/bnx2x-e1h-7.10.51.0.fw
File: bnx2x/bnx2x-e2-7.10.51.0.fw
File: bnx2x/bnx2x-e1-7.12.30.0.fw
File: bnx2x/bnx2x-e1h-7.12.30.0.fw
File: bnx2x/bnx2x-e2-7.12.30.0.fw
File: bnx2x/bnx2x-e1-7.13.1.0.fw
File: bnx2x/bnx2x-e1h-7.13.1.0.fw
File: bnx2x/bnx2x-e2-7.13.1.0.fw

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Driver: bnx2 - Broadcom NetXtremeII

File: bnx2/bnx2-mips-06-4.6.16.fw
File: bnx2/bnx2-mips-06-5.0.0.j3.fw
File: bnx2/bnx2-mips-06-5.0.0.j6.fw
File: bnx2/bnx2-mips-06-6.0.15.fw
File: bnx2/bnx2-mips-06-6.2.1.fw
File: bnx2/bnx2-mips-06-6.2.3.fw
File: bnx2/bnx2-mips-09-4.6.17.fw
File: bnx2/bnx2-mips-09-5.0.0.j3.fw
File: bnx2/bnx2-mips-09-5.0.0.j9.fw
File: bnx2/bnx2-mips-09-5.0.0.j15.fw
File: bnx2/bnx2-mips-09-6.0.17.fw
File: bnx2/bnx2-mips-09-6.2.1.fw
File: bnx2/bnx2-mips-09-6.2.1a.fw
File: bnx2/bnx2-mips-09-6.2.1b.fw
File: bnx2/bnx2-rv2p-06-4.6.16.fw
File: bnx2/bnx2-rv2p-06-5.0.0.j3.fw
File: bnx2/bnx2-rv2p-06-6.0.15.fw
File: bnx2/bnx2-rv2p-09-4.6.15.fw
File: bnx2/bnx2-rv2p-09-5.0.0.j3.fw
File: bnx2/bnx2-rv2p-09-5.0.0.j10.fw
File: bnx2/bnx2-rv2p-09-6.0.17.fw
File: bnx2/bnx2-rv2p-09ax-5.0.0.j3.fw
File: bnx2/bnx2-rv2p-09ax-5.0.0.j10.fw
File: bnx2/bnx2-rv2p-09ax-6.0.17.fw

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Driver: netxen_nic - NetXen Multi port (1/10) Gigabit Ethernet NIC

File: phanfw.bin
Version: 4.0.590

Licence: Redistributable. See LICENCE.phanfw for details.

Available from http://ldriver.qlogic.com/firmware/netxen_nic/new/

Driver: dvb-ttpci -- AV7110 cards

File: av7110/bootcode.bin
Source: av7110/Boot.S
Source: av7110/Makefile

Licence: GPLv2 or later. See GPL-2 and GPL-3 for details.

ARM assembly source code from <https://linuextv.org/downloads/firmware/Boot.S>

Driver: snd-wavefront - ISA WaveFront sound card

File: yamaha/yss225_registers.bin

Licence: Allegedly GPLv2+, but no source visible.

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Driver: rt61pci - Ralink RT2561, RT2561S, RT2661 wireless MACs

File: rt2561.bin

File: rt2561s.bin

File: rt2661.bin

Licence: Redistributable. See LICENCE.ralink-firmware.txt for details

Downloaded from <http://www.ralinktech.com/ralink/Home/Support/Linux.html>

Driver: rt73usb - Ralink RT2571W, RT2671 wireless MACs

File: rt73.bin

Licence: Redistributable. See LICENCE.ralink-firmware.txt for details

Downloaded from <http://www.ralinktech.com/ralink/Home/Support/Linux.html>

Driver: mt7601u - MediaTek MT7601U Wireless MACs

File: mt7601u.bin

Version: 34

Licence: Redistributable. See LICENCE.ralink_a_mEDIATEK_company_firmware for details

Downloaded from <http://www.mediatek.com/en/downloads/>

Driver: rt2800pci - Ralink RT2860, RT2890, RT3090, RT3290, RT5390 wireless MACs

File: rt2860.bin

Version: 40

File: rt3290.bin

Version: 37

Licence: Redistributable. See LICENCE.ralink-firmware.txt for details

Binary file supplied originally by Shiang Tu <shiang_tu@ralinktech.com>, latest

from <http://www.mediatek.com/en/downloads1/downloads/>

Driver: rt2860sta - Ralink RT3090 wireless MACs

Link: rt3090.bin -> rt2860.bin

Licence: Redistributable. See LICENCE.ralink-firmware.txt for details

Driver: rt2800usb - Ralink RT2870, RT3070, RT3071, RT3072, RT5370 wireless MACs

File: rt2870.bin

Version: 36

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Binary file supplied originally by Shiang Tu <shiang_tu@ralinktech.com>, latest from <http://www.mediatek.com/en/downloads1/downloads/>

Driver: rt2870sta - Ralink RT2870, RT3070, RT3071 wireless MACs

Link: rt3070.bin -> rt2870.bin

File: rt3071.bin

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rt3071.bin is a copy of bytes 4096-8191 of rt2870.bin for compatibility.

Driver: usbdux/usbduxfast/usbduxsigma - usbdux data acquisition cards

File: usbdux_firmware.bin

File: usbduxfast_firmware.bin

File: usbduxsigma_firmware.bin

Source: usbdux/

Licence: GPLv2. See GPL-2 for details.

Provided from the author, Bernd Porr <BerndPorr@f2s.com>

Driver: xc4000 - Xceive 4000 Tuner driver

File: dvb-fe-xc4000-1.4.1.fw

Version: 1.4.1

Licence: Redistributable. See LICENCE.xc4000 for details

Driver: xc5000 - Xceive 5000 Tuner driver

File: dvb-fe-xc5000-1.6.114.fw

Version: 1.6.114

File: dvb-fe-xc5000c-4.1.30.7.fw
Version: 4.1.30.7

Licence: Redistributable. See LICENCE.xc5000 and LICENCE.xc5000c for details

Driver: dib0700 - DiBcom dib0700 USB DVB bridge driver

File: dvb-usb-dib0700-1.20.fw
Version: 1.20

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Driver: ath3k - DFU Driver for Atheros bluetooth chipset AR3011

File: ath3k-1.fw
Version: 1.0

Fix EEPROM radio table issue and change PID to 3005

Licence: Redistributable. See LICENCE.atheros_firmware for details

Driver: mga - Matrox G200/G400/G550

File: matrox/g200_warp.fw
File: matrox/g400_warp.fw

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Driver: r128 - ATI Rage 128

File: r128/r128_cce.bin

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Driver: radeon - ATI Radeon

File: radeon/R100_cp.bin
File: radeon/R200_cp.bin
File: radeon/R300_cp.bin
File: radeon/R420_cp.bin
File: radeon/RS600_cp.bin
File: radeon/RS690_cp.bin
File: radeon/R520_cp.bin
File: radeon/R600_pfp.bin
File: radeon/R600_me.bin
File: radeon/RV610_pfp.bin
File: radeon/RV610_me.bin
File: radeon/RV630_pfp.bin
File: radeon/RV630_me.bin
File: radeon/RV620_pfp.bin
File: radeon/RV620_me.bin
File: radeon/RV635_pfp.bin
File: radeon/RV635_me.bin
File: radeon/RV670_pfp.bin
File: radeon/RV670_me.bin
File: radeon/RS780_pfp.bin
File: radeon/RS780_me.bin
File: radeon/RV770_pfp.bin
File: radeon/RV770_me.bin
File: radeon/RV730_pfp.bin
File: radeon/RV730_me.bin
File: radeon/RV710_pfp.bin
File: radeon/RV710_me.bin

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Driver: radeon - ATI Radeon

File: radeon/R600_rlc.bin
File: radeon/R600_uvd.bin
File: radeon/RS780_uvd.bin
File: radeon/R700_rlc.bin
File: radeon/RV710_uvd.bin
File: radeon/RV710_smc.bin
File: radeon/RV730_smc.bin
File: radeon/RV740_smc.bin
File: radeon/RV770_smc.bin
File: radeon/RV770_uvd.bin
File: radeon/CEDAR_me.bin
File: radeon/CEDAR_pfp.bin
File: radeon/CEDAR_qlc.bin
File: radeon/CEDAR_smc.bin
File: radeon/CYPRESS_me.bin
File: radeon/CYPRESS_pfp.bin
File: radeon/CYPRESS_qlc.bin
File: radeon/CYPRESS_uvd.bin
File: radeon/CYPRESS_smc.bin
File: radeon/JUNIPER_me.bin
File: radeon/JUNIPER_pfp.bin
File: radeon/JUNIPER_qlc.bin
File: radeon/JUNIPER_smc.bin
File: radeon/REDWOOD_me.bin
File: radeon/REDWOOD_pfp.bin
File: radeon/REDWOOD_qlc.bin
File: radeon/REDWOOD_smc.bin
File: radeon/PALM_me.bin
File: radeon/PALM_pfp.bin

File: radeon/SUMO_rlc.bin
File: radeon/SUMO_uvd.bin
File: radeon/BARTS_mc.bin
File: radeon/BARTS_me.bin
File: radeon/BARTS_pfp.bin
File: radeon/BARTS_smc.bin
File: radeon/BTC_rlc.bin
File: radeon/CAICOS_mc.bin
File: radeon/CAICOS_me.bin
File: radeon/CAICOS_pfp.bin
File: radeon/CAICOS_smc.bin
File: radeon/TURKS_mc.bin
File: radeon/TURKS_me.bin
File: radeon/TURKS_pfp.bin
File: radeon/TURKS_smc.bin
File: radeon/CAYMAN_mc.bin
File: radeon/CAYMAN_me.bin
File: radeon/CAYMAN_pfp.bin
File: radeon/CAYMAN_rlc.bin
File: radeon/CAYMAN_smc.bin
File: radeon/SUMO_pfp.bin
File: radeon/SUMO_me.bin
File: radeon/SUMO2_pfp.bin
File: radeon/SUMO2_me.bin
File: radeon/ARUBA_me.bin
File: radeon/ARUBA_pfp.bin
File: radeon/ARUBA_rlc.bin
File: radeon/PITCAIRN_ce.bin
File: radeon/PITCAIRN_mc.bin
File: radeon/PITCAIRN_mc2.bin
File: radeon/PITCAIRN_me.bin
File: radeon/PITCAIRN_pfp.bin
File: radeon/PITCAIRN_rlc.bin
File: radeon/PITCAIRN_smc.bin
File: radeon/TAHITI_ce.bin
File: radeon/TAHITI_mc.bin
File: radeon/TAHITI_mc2.bin
File: radeon/TAHITI_me.bin
File: radeon/TAHITI_pfp.bin
File: radeon/TAHITI_rlc.bin
File: radeon/TAHITI_uvd.bin
File: radeon/TAHITI_smc.bin
File: radeon/TAHITI_vce.bin
File: radeon/VERDE_ce.bin
File: radeon/VERDE_mc.bin
File: radeon/VERDE_mc2.bin
File: radeon/VERDE_me.bin
File: radeon/VERDE_pfp.bin
File: radeon/VERDE_rlc.bin
File: radeon/VERDE_smc.bin
File: radeon/OLAND_ce.bin
File: radeon/OLAND_mc.bin
File: radeon/OLAND_mc2.bin
File: radeon/OLAND_me.bin
File: radeon/OLAND_pfp.bin
File: radeon/OLAND_rlc.bin
File: radeon/OLAND_smc.bin
File: radeon/HAINAN_ce.bin
File: radeon/HAINAN_mc.bin
File: radeon/HAINAN_mc2.bin

File: radeon/HAINAN_me.bin
File: radeon/HAINAN_pfp.bin
File: radeon/HAINAN_rlc.bin
File: radeon/HAINAN_smc.bin
File: radeon/BONAIRES_ce.bin
File: radeon/BONAIRES_mc.bin
File: radeon/BONAIRES_mc2.bin
File: radeon/BONAIRES_me.bin
File: radeon/BONAIRES_mec.bin
File: radeon/BONAIRES_pfp.bin
File: radeon/BONAIRES_rlc.bin
File: radeon/BONAIRES_sdma.bin
File: radeon/BONAIRES_uvd.bin
File: radeon/BONAIRES_smc.bin
File: radeon/BONAIRES_vce.bin
File: radeon/KABINI_ce.bin
File: radeon/KABINI_me.bin
File: radeon/KABINI_mec.bin
File: radeon/KABINI_pfp.bin
File: radeon/KABINI_rlc.bin
File: radeon/KABINI_sdma.bin
File: radeon/KAVERI_ce.bin
File: radeon/KAVERI_me.bin
File: radeon/KAVERI_mec.bin
File: radeon/KAVERI_pfp.bin
File: radeon/KAVERI_rlc.bin
File: radeon/KAVERI_sdma.bin
File: radeon/HAWAII_ce.bin
File: radeon/HAWAII_mc.bin
File: radeon/HAWAII_mc2.bin
File: radeon/HAWAII_me.bin
File: radeon/HAWAII_mec.bin
File: radeon/HAWAII_pfp.bin
File: radeon/HAWAII_rlc.bin
File: radeon/HAWAII_sdma.bin
File: radeon/HAWAII_smc.bin
File: radeon/MULLINS_ce.bin
File: radeon/MULLINS_me.bin
File: radeon/MULLINS_mec.bin
File: radeon/MULLINS_pfp.bin
File: radeon/MULLINS_rlc.bin
File: radeon/MULLINS_sdma.bin
File: radeon/pitcairn_ce.bin
File: radeon/pitcairn_k_smc.bin
File: radeon/pitcairn_mc.bin
File: radeon/pitcairn_me.bin
File: radeon/pitcairn_pfp.bin
File: radeon/pitcairn_rlc.bin
File: radeon/pitcairn_smc.bin
File: radeon/tahiti_ce.bin
File: radeon/tahiti_k_smc.bin
File: radeon/tahiti_mc.bin
File: radeon/tahiti_me.bin
File: radeon/tahiti_pfp.bin
File: radeon/tahiti_rlc.bin
File: radeon/tahiti_smc.bin
File: radeon/verde_ce.bin
File: radeon/verde_k_smc.bin
File: radeon/verde_mc.bin
File: radeon/verde_me.bin

File: radeon/verde_pfp.bin
File: radeon/verde_rlc.bin
File: radeon/verde_smc.bin
File: radeon/oland_ce.bin
File: radeon/oland_k_smc.bin
File: radeon/oland_mc.bin
File: radeon/oland_me.bin
File: radeon/oland_pfp.bin
File: radeon/oland_rlc.bin
File: radeon/oland_smc.bin
File: radeon/hainan_ce.bin
File: radeon/hainan_k_smc.bin
File: radeon/hainan_mc.bin
File: radeon/hainan_me.bin
File: radeon/hainan_pfp.bin
File: radeon/hainan_rlc.bin
File: radeon/hainan_smc.bin
File: radeon/bonaire_ce.bin
File: radeon/bonaire_k_smc.bin
File: radeon/bonaire_mc.bin
File: radeon/bonaire_me.bin
File: radeon/bonaire_mec.bin
File: radeon/bonaire_pfp.bin
File: radeon/bonaire_rlc.bin
File: radeon/bonaire_sdma.bin
File: radeon/bonaire_sdma1.bin
File: radeon/bonaire_smc.bin
File: radeon/bonaire_uvd.bin
File: radeon/bonaire_vce.bin
File: radeon/kabini_ce.bin
File: radeon/kabini_me.bin
File: radeon/kabini_mec.bin
File: radeon/kabini_pfp.bin
File: radeon/kabini_rlc.bin
File: radeon/kabini_sdma.bin
File: radeon/kabini_sdma1.bin
File: radeon/kabini_uvd.bin
File: radeon/kabini_vce.bin
File: radeon/kaveri_ce.bin
File: radeon/kaveri_me.bin
File: radeon/kaveri_mec.bin
File: radeon/kaveri_mec2.bin
File: radeon/kaveri_pfp.bin
File: radeon/kaveri_rlc.bin
File: radeon/kaveri_sdma.bin
File: radeon/kaveri_sdma1.bin
File: radeon/kaveri_uvd.bin
File: radeon/kaveri_vce.bin
File: radeon/hawaii_ce.bin
File: radeon/hawaii_k_smc.bin
File: radeon/hawaii_mc.bin
File: radeon/hawaii_me.bin
File: radeon/hawaii_mec.bin
File: radeon/hawaii_pfp.bin
File: radeon/hawaii_rlc.bin
File: radeon/hawaii_sdma.bin
File: radeon/hawaii_sdma1.bin
File: radeon/hawaii_smc.bin
File: radeon/hawaii_uvd.bin
File: radeon/hawaii_vce.bin

File: radeon/mullins_ce.bin
File: radeon/mullins_me.bin
File: radeon/mullins_mec.bin
File: radeon/mullins_pfp.bin
File: radeon/mullins_rlc.bin
File: radeon/mullins_sdma.bin
File: radeon/mullins_sdma1.bin
File: radeon/mullins_uvd.bin
File: radeon/mullins_vce.bin

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Driver: amdgpu - AMD Radeon

File: amdgpu/topaz_ce.bin
File: amdgpu/topaz_k_smc.bin
File: amdgpu/topaz_mc.bin
File: amdgpu/topaz_me.bin
File: amdgpu/topaz_mec2.bin
File: amdgpu/topaz_mec.bin
File: amdgpu/topaz_pfp.bin
File: amdgpu/topaz_rlc.bin
File: amdgpu/topaz_sdma1.bin
File: amdgpu/topaz_sdma.bin
File: amdgpu/topaz_smc.bin
File: amdgpu/tonga_ce.bin
File: amdgpu/tonga_k_smc.bin
File: amdgpu/tonga_mc.bin
File: amdgpu/tonga_me.bin
File: amdgpu/tonga_mec2.bin
File: amdgpu/tonga_mec.bin
File: amdgpu/tonga_pfp.bin
File: amdgpu/tonga_rlc.bin
File: amdgpu/tonga_sdma1.bin
File: amdgpu/tonga_sdma.bin
File: amdgpu/tonga_smc.bin
File: amdgpu/tonga_uvd.bin
File: amdgpu/tonga_vce.bin
File: amdgpu/carrizo_ce.bin
File: amdgpu/carrizo_me.bin
File: amdgpu/carrizo_mec2.bin
File: amdgpu/carrizo_mec.bin
File: amdgpu/carrizo_pfp.bin
File: amdgpu/carrizo_rlc.bin
File: amdgpu/carrizo_sdma1.bin
File: amdgpu/carrizo_sdma.bin
File: amdgpu/carrizo_uvd.bin
File: amdgpu/carrizo_vce.bin
File: amdgpu/fiji_ce.bin
File: amdgpu/fiji_mc.bin
File: amdgpu/fiji_me.bin
File: amdgpu/fiji_mec2.bin
File: amdgpu/fiji_mec.bin
File: amdgpu/fiji_pfp.bin
File: amdgpu/fiji_rlc.bin
File: amdgpu/fiji_sdma1.bin
File: amdgpu/fiji_sdma.bin
File: amdgpu/fiji_smc.bin

File: amdgpu/fiji_uvd.bin
File: amdgpu/fiji_vce.bin
File: amdgpu/stoney_ce.bin
File: amdgpu/stoney_me.bin
File: amdgpu/stoney_mec.bin
File: amdgpu/stoney_pfp.bin
File: amdgpu/stoney_rlc.bin
File: amdgpu/stoney_sdma.bin
File: amdgpu/stoney_uvd.bin
File: amdgpu/stoney_vce.bin
File: amdgpu/polaris10_ce.bin
File: amdgpu/polaris10_mc.bin
File: amdgpu/polaris10_me.bin
File: amdgpu/polaris10_mec2.bin
File: amdgpu/polaris10_mec.bin
File: amdgpu/polaris10_pfp.bin
File: amdgpu/polaris10_rlc.bin
File: amdgpu/polaris10_sdma1.bin
File: amdgpu/polaris10_sdma.bin
File: amdgpu/polaris10_smc.bin
File: amdgpu/polaris10_k_smc.bin
File: amdgpu/polaris10_smc_sk.bin
File: amdgpu/polaris10_uvd.bin
File: amdgpu/polaris10_vce.bin
File: amdgpu/polaris11_ce.bin
File: amdgpu/polaris11_mc.bin
File: amdgpu/polaris11_me.bin
File: amdgpu/polaris11_mec2.bin
File: amdgpu/polaris11_mec.bin
File: amdgpu/polaris11_pfp.bin
File: amdgpu/polaris11_rlc.bin
File: amdgpu/polaris11_sdma1.bin
File: amdgpu/polaris11_sdma.bin
File: amdgpu/polaris11_smc.bin
File: amdgpu/polaris11_k_smc.bin
File: amdgpu/polaris11_smc_sk.bin
File: amdgpu/polaris11_uvd.bin
File: amdgpu/polaris11_vce.bin
File: amdgpu/polaris12_ce.bin
File: amdgpu/polaris12_mc.bin
File: amdgpu/polaris12_me.bin
File: amdgpu/polaris12_mec.bin
File: amdgpu/polaris12_mec2.bin
File: amdgpu/polaris12_pfp.bin
File: amdgpu/polaris12_rlc.bin
File: amdgpu/polaris12_sdma.bin
File: amdgpu/polaris12_sdma1.bin
File: amdgpu/polaris12_smc.bin
File: amdgpu/polaris12_uvd.bin
File: amdgpu/polaris12_vce.bin

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Driver: s2255drv

File: f2255usb.bin

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Driver: rtl8192e - Realtek 8192 PCI wireless driver

File: RTL8192E/boot.img
File: RTL8192E/data.img
File: RTL8192E/main.img

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Driver: lgs8gxx - Legend Silicon GB20600 demodulator driver

File: lgs8g75.fw

Licence: Unknown

Driver: ib_qib - QLogic Infiniband

File: qllogic/sd7220.fw

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Found in hex form in kernel source.

Driver: qed - QLogic 4xxxx Ethernet Driver Core Module.

File: qed/qed_init_values_zipped-8.4.2.0.bin
File: qed/qed_init_values_zipped-8.7.3.0.bin
File: qed/qed_init_values_zipped-8.10.5.0.bin
File: qed/qed_init_values_zipped-8.10.10.0.bin
File: qed/qed_init_values_zipped-8.15.3.0.bin

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Driver: ueagle-atm - Driver for USB ADSL Modems based on Eagle IV Chipset

File: ueagle-atm/CMV4p.bin.v2
File: ueagle-atm/DSP4p.bin
File: ueagle-atm/eagleIV.fw
Version: 1.0

Licence: Redistributable. See LICENCE.ueagle-atm4-firmware for details

Driver: ueagle-atm - Driver for USB ADSL Modems based on Eagle I,II,III

File: ueagle-atm/930-fpga.bin
File: ueagle-atm/CMVeiWO.bin
File: ueagle-atm/CMVepFR10.bin
File: ueagle-atm/DSP9p.bin
File: ueagle-atm/eagleIII.fw
File: ueagle-atm/adi930.fw
File: ueagle-atm/CMVep.bin
File: ueagle-atm/CMVepFR.bin
File: ueagle-atm/DSPei.bin
File: ueagle-atm/CMV9i.bin
File: ueagle-atm/CMVepES03.bin
File: ueagle-atm/CMVepIT.bin
File: ueagle-atm/DSPep.bin
File: ueagle-atm/CMV9p.bin
File: ueagle-atm/CMVepES.bin
File: ueagle-atm/CMVepWO.bin
File: ueagle-atm/eagleI.fw
File: ueagle-atm/CMVei.bin

File: ueagle-atm/CMVepFR04.bin
File: ueagle-atm/DSP9i.bin
File: ueagle-atm/eagleII.fw
Version: 1.1

Licence: Redistributable. Based on
<https://mail.gna.org/public/eagleusb-dev/2004-11/msg00172.html>

Driver: vxge - Exar X3100 Series 10GbE PCIe I/O Virtualized Server Adapter

File: vxge/X3fw.ncf
File: vxge/X3fw-pxe.ncf
Version: 1.8.1

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Driver: brcmsmac - Broadcom 802.11n softmac wireless LAN driver.

File: brcm/bcm43xx-0.fw
File: brcm/bcm43xx_hdr-0.fw
Version: 610.812

Licence: Redistributable. See LICENCE.broadcom_bcm43xx for details.

Driver: brcmfmac - Broadcom 802.11n fullmac wireless LAN driver.

File: brcm/bcm4329-fullmac-4.bin
File: brcm/brcmfmac43236b.bin
File: brcm/brcmfmac4329-sdio.bin
File: brcm/brcmfmac4330-sdio.bin
File: brcm/brcmfmac4334-sdio.bin
File: brcm/brcmfmac43340-sdio.bin
File: brcm/brcmfmac4335-sdio.bin
File: brcm/brcmfmac43362-sdio.bin
File: brcm/brcmfmac4339-sdio.bin
File: brcm/brcmfmac43241b0-sdio.bin
File: brcm/brcmfmac43241b4-sdio.bin
File: brcm/brcmfmac43241b5-sdio.bin
File: brcm/brcmfmac43242a.bin
File: brcm/brcmfmac43143.bin
File: brcm/brcmfmac43143-sdio.bin
File: brcm/brcmfmac43430-sdio.bin
File: brcm/brcmfmac43455-sdio.bin
File: brcm/brcmfmac4350c2-pcie.bin
File: brcm/brcmfmac4350-pcie.bin
File: brcm/brcmfmac4354-sdio.bin
File: brcm/brcmfmac4356-pcie.bin

File: brcm/brcmfmac4356-sdio.bin
File: brcm/brcmfmac43569.bin
File: brcm/brcmfmac43570-pcie.bin
File: brcm/brcmfmac4358-pcie.bin
File: brcm/brcmfmac43602-pcie.bin
File: brcm/brcmfmac43602-pcie.ap.bin
File: brcm/brcmfmac4366b-pcie.bin
File: brcm/brcmfmac4371-pcie.bin

Licence: Redistributable. See LICENCE.broadcom_bcm43xx for details.

Driver: tda7706 - FM radio Highly integrated tuner for AM/FM car-radio

File: TDA7706_OM_v2.5.1_boot.txt
File: TDA7706_OM_v3.0.2_boot.txt

Licence: Redistributable. See LICENCE.tda7706-firmware.txt for details.
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Driver: wl1251 - Texas Instruments 802.11 WLAN driver for WiLink4 chips

File: ti-connectivity/wl1251-fw.bin
Version: 4.0.4.3.7

File: ti-connectivity/wl1251-nvs.bin

Licence: Redistributable. See LICENCE.wl1251 for details.

The published NVS files are for testing only. Every device needs to have a unique NVS which is properly calibrated for best results.

The driver expects to find the firmwares under a ti-connectivity subdirectory. So if your system looks for firmwares in /lib/firmware, the firmwares for wl12xx chips must be located in /lib/firmware/ti-connectivity/.

Driver: wl12xx - Texas Instruments 802.11 WLAN driver for WiLink6/7 chips

File: ti-connectivity/wl1271-fw.bin
Version: 6.1.0.50.350 (STA-only)
File: ti-connectivity/wl1271-fw-2.bin
Version: 6.1.5.50.74 (STA-only)
File: ti-connectivity/wl1271-fw-ap.bin
Version: 6.2.1.0.54 (AP-only)
File: ti-connectivity/wl127x-fw-3.bin
Version: 6.3.0.0.77
File: ti-connectivity/wl127x-fw-plt-3.bin
Version: 6.3.0.0.77 (PLT-only)
File: ti-connectivity/wl127x-fw-4-sr.bin
Version: 6.3.5.0.98 (Single-role)
File: ti-connectivity/wl127x-fw-4-mr.bin
Version: 6.5.2.0.15 (Multi-role)
File: ti-connectivity/wl127x-fw-4-plt.bin
Version: 6.3.5.0.98 (PLT-only)
File: ti-connectivity/wl127x-fw-5-sr.bin

Version: 6.3.10.0.141 (Single-role)
File: ti-connectivity/wl127x-fw-5-mr.bin
Version: 6.5.7.0.49 (Multi-role)
File: ti-connectivity/wl127x-fw-5-plt.bin
Version: 6.3.10.0.141 (PLT-only)

File: ti-connectivity/wl128x-fw.bin
Version: 7.1.5.50.74 (STA-only)
File: ti-connectivity/wl128x-fw-ap.bin
Version: 7.2.1.0.54 (AP-only)
File: ti-connectivity/wl128x-fw-3.bin
Version: 7.3.0.0.77
File: ti-connectivity/wl128x-fw-plt-3.bin
Version: 7.3.0.0.77
File: ti-connectivity/wl128x-fw-4-sr.bin
Version: 7.3.5.0.98 (Single-role)
File: ti-connectivity/wl128x-fw-4-mr.bin
Version: 7.5.2.0.15 (Multi-role)
File: ti-connectivity/wl128x-fw-4-plt.bin
Version: 7.3.5.0.98 (PLT)
File: ti-connectivity/wl128x-fw-5-sr.bin
Version: 7.3.10.0.141 (Single-role)
File: ti-connectivity/wl128x-fw-5-mr.bin
Version: 7.5.7.0.49 (Multi-role)
File: ti-connectivity/wl128x-fw-5-plt.bin
Version: 7.3.10.2.141 (PLT-only)

File: ti-connectivity/wl127x-nvs.bin
File: ti-connectivity/wl128x-nvs.bin
Link: ti-connectivity/wl12xx-nvs.bin -> wl127x-nvs.bin
Link: ti-connectivity/wl1271-nvs.bin -> wl127x-nvs.bin

Licence: Redistributable. See LICENCE.ti-connectivity for details.

The NVS file includes two parts:
- radio calibration
- HW configuration parameters (aka. INI values)

The published NVS files are for testing only. Every device needs to have a unique NVS which is properly calibrated for best results. You can find more information about NVS generation for your device here:

<http://wireless.kernel.org/en/users/Drivers/wl12xx/calibrator>

If you're using a wl127x based device, use a symbolic link called wl1271-nvs.bin that links to the wl127x-nvs.bin file. If you are using wl128x, link to wl128x-nvs.bin instead.

The driver expects to find the firmwares under a ti-connectivity subdirectory. So if your system looks for firmwares in /lib/firmware, the firmwares for wl12xx chips must be located in /lib/firmware/ti-connectivity/.

Driver: wl18xx - Texas Instruments 802.11 WLAN driver for WiLink8 chips

File: ti-connectivity/wl18xx-fw.bin
Version: 8.2.0.0.100
File: ti-connectivity/wl18xx-fw-2.bin

Version: 8.5.0.0.55
File: ti-connectivity/wl18xx-fw-3.bin
Version: 8.8.0.0.13
File: ti-connectivity/wl18xx-fw-4.bin
Version: 8.9.0.0.69

Licence: Redistributable. See LICENCE.ti-connectivity for details.

The driver expects to find the firmwares under a ti-connectivity subdirectory. So if your system looks for firmwares in /lib/firmware, the firmwares for wl18xx chips must be located in /lib/firmware/ti-connectivity/.

Driver: TI_ST - Texas Instruments bluetooth driver

File: ti-connectivity/TIinit_7.2.31.bts

Licence: Redistributable. See LICENCE.ti-connectivity for details.

TIinit_7.2.31.bts version 7.2.31

In order to use that file copy it to /lib/firmware/ti-connectivity.

Driver: tlg2300 - Telgent 2300 V4L/DVB driver.

File: tlg2300_firmware.bin

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Driver: r8712u - Realtek 802.11n WLAN driver for RTL8712U

File: rtlwifi/rtl8712u.bin

Info: From Vendor's rtl8712_8188_8191_8192SU_usb_linux_v7_0.20100831
Reverted rtl8188C_8192C_8192D_usb_linux_v3.4.2_3727.20120404

Licence: Redistributable. See LICENCE.rtlwifi_firmware.txt for details.

Driver: rtl8192ce - Realtek 802.11n WLAN driver for RTL8192CE

File: rtlwifi/rtl8192cfw.bin

File: rtlwifi/rtl8192cfwU.bin

File: rtlwifi/rtl8192cfwU_B.bin

Info: From Vendor's realtek/rtlwifi_linux_mac80211_0019.0320.2014V628 driver

Licence: Redistributable. See LICENCE.rtlwifi_firmware.txt for details.

Driver: rtl8192cu - Realtek 802.11n WLAN driver for RTL8192CU

File: rtlwifi/rtl8192cufw.bin
File: rtlwifi/rtl8192cufw_A.bin
File: rtlwifi/rtl8192cufw_B.bin
File: rtlwifi/rtl8192cufw_TMSC.bin

Licence: Redistributable. See LICENCE.rtlwifi_firmware.txt for details.

Driver: rtl8192se - Realtek 802.11n WLAN driver for RTL8192SE

Info: updated from rtl_92ce_92se_92de_linux_mac80211_0004.0816.2011 driver version
File: rtlwifi/rtl8192sefw.bin

Licence: Redistributable. See LICENCE.rtlwifi_firmware.txt for details.

Driver: rtl8192de - Realtek 802.11n WLAN driver for RTL8192DE

Info: Updated from Realtek version rtl_92ce_92se_92de_8723ae_linux_mac80211_0007.0809.2012
File: rtlwifi/rtl8192defw.bin

Licence: Redistributable. See LICENCE.rtlwifi_firmware.txt for details.

Driver: rtl8723e - Realtek 802.11n WLAN driver for RTL8723E

Info: Taken from Realtek version rtl_92ce_92se_92de_8723ae_linux_mac80211_0007.0809.2012
File: rtlwifi/rtl8723fw.bin
File: rtlwifi/rtl8723fw_B.bin

Licence: Redistributable. See LICENCE.rtlwifi_firmware.txt for details.

Driver: rtl8723be - Realtek 802.11n WLAN driver for RTL8723BE

Info: From Vendor's realtek/rtlwifi_linux_mac80211_0019.0320.2014V628 driver
File: rtlwifi/rtl8723befw.bin
Info: Update to version 36 - Sent by Realtek
File: rtlwifi/rtl8723befw_36.bin

Licence: Redistributable. See LICENCE.rtlwifi_firmware.txt for details.

Driver: r8723au - Realtek 802.11n WLAN driver for RTL8723AU

Info: Taken from Realtek driver rtl8723A_WiFi_linux_v4.1.3_6044.20121224
Firmware is embedded in the driver as data statements. This info
has been extracted into a binary file.

File: rtlwifi/rtl8723aufw_A.bin
File: rtlwifi/rtl8723aufw_B.bin
File: rtlwifi/rtl8723aufw_B_NoBT.bin

Licence: Redistributable. See LICENCE.rtlwifi_firmware.txt for details.

Driver: rtl8188ee - Realtek 802.11n WLAN driver for RTL8188EE

Info: Taken from Realtek version rtl_92ce_92se_92de_8723ae_88ee_linux_mac80211_0010.0109.2013
File: rtlwifi/rtl8188efw.bin

Licence: Redistributable. See LICENCE.rtlwifi_firmware.txt for details.

Driver: rtl8188eu - Realtek 802.11n WLAN driver for RTL8188EU

Info: Taken from Realtek version RTL8188EUS_linux_v4.1.4_6773.20130222
File: rtlwifi/rtl8188eufw.bin

Licence: Redistributable. See LICENCE.rtlwifi_firmware.txt for details.

Driver: rtl8821ae - Realtek 802.11n WLAN driver for RTL8821AE

Info: From Vendor's realtek/rtlwifi_linux_mac80211_0019.0320.2014V628 driver
File: rtlwifi/rtl8821aefw.bin
File: rtlwifi/rtl8821aefw_wowlan.bin
Info: Update to version 29 - Sent by Realtek
File: rtlwifi/rtl8821aefw_29.bin

Licence: Redistributable. See LICENCE.rtlwifi_firmware.txt for details.

Driver: rtl8192ee - Realtek 802.11n WLAN driver for RTL8192EE

Info: Initial version taken from Realtek version
rtl_92ce_92se_92de_8723ae_88ee_8723be_92ee_linux_mac80211_0017.1224.2013
Updated Jan. 14, 2015 with file added by Realtek to
http://github.com/lwfinger/rtlwifi_new.git.
File: rtlwifi/rtl8192eefw.bin

Licence: Redistributable. See LICENCE.rtlwifi_firmware.txt for details.

Driver: rtl8723bs - Realtek 802.11n WLAN driver for RTL8723BS

Info: Firmware files extracted from data statements in Realtek driver
v4.3.5.5_12290.20140916_BTCOEX20140507-4E40.
File: rtlwifi/rtl8723bs_bt.bin
File: rtlwifi/rtl8723bs_nic.bin
File: rtlwifi/rtl8723bs_ap_wowlan.bin
File: rtlwifi/rtl8723bs_wowlan.bin

Licence: Redistributable. See LICENCE.rtlwifi_firmware.txt for details.

Driver: rtl8xxxu - Realtek 802.11n WLAN driver for RTL8XXX USB devices

Info: rtl8723au taken from Realtek driver
rtl8723A_WiFi_linux_v4.1.3_6044.20121224
Firmware is embedded in the driver as data statements. This info
has been extracted into a binary file.
File: rtlwifi/rtl8723aufw_A.bin
File: rtlwifi/rtl8723aufw_B.bin
File: rtlwifi/rtl8723aufw_B_NoBT.bin

Info: rtl8723bu taken from Realtek driver
rtl8723BU_WiFi_linux_v4.3.16_14189.20150519_BTCEX20150119-5844
Firmware is embedded in the driver as data statements. This info
has been extracted into a binary file.
File: rtlwifi/rtl8723bu_nic.bin
File: rtlwifi/rtl8723bu_wowlan.bin
File: rtlwifi/rtl8723bu_ap_wowlan.bin

Info: rtl8192eu taken from Realtek driver
rtl8192EU_linux_v4.3.1.1_11320.20140505
Firmware is embedded in the driver as data statements. This info
has been extracted into a binary file.
File: rtlwifi/rtl8192eu_nic.bin
File: rtlwifi/rtl8192eu_wowlan.bin
File: rtlwifi/rtl8192eu_ap_wowlan.bin

Licence: Redistributable. See LICENCE.rtlwifi_firmware.txt for details.

Driver: r8169 - RealTek 8169/8168/8101 ethernet driver.

File: rtl_nic/rtl8168d-1.fw
File: rtl_nic/rtl8168d-2.fw
File: rtl_nic/rtl8105e-1.fw
File: rtl_nic/rtl8168e-1.fw
File: rtl_nic/rtl8168e-2.fw

File: rtl_nic/rtl8168e-3.fw
Version: 0.0.4

File: rtl_nic/rtl8168f-1.fw
Version: 0.0.5

File: rtl_nic/rtl8168f-2.fw
Version: 0.0.4

File: rtl_nic/rtl8411-1.fw
Version: 0.0.3

File: rtl_nic/rtl8411-2.fw
Version: 0.0.1

File: rtl_nic/rtl8402-1.fw
Version: 0.0.1

File: rtl_nic/rtl8106e-1.fw

Version: 0.0.1

File: rtl_nic/rtl8106e-2.fw
Version: 0.0.1

File: rtl_nic/rtl8168g-1.fw
Version: 0.0.3

File: rtl_nic/rtl8168g-2.fw
Version: 0.0.1

File: rtl_nic/rtl8168g-3.fw
Version: 0.0.1

File: rtl_nic/rtl8168h-1.fw
Version: 0.0.2

File: rtl_nic/rtl8168h-2.fw
Version: 0.0.2

File: rtl_nic/rtl8107e-1.fw
Version: 0.0.2

File: rtl_nic/rtl8107e-2.fw
Version: 0.0.2

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Driver: vt6656 - VIA VT6656 USB wireless driver

File: vntwusb.fw

Licence: Redistributable. See LICENCE.via_vt6656 for details.

Driver: DFU Driver for Atheros bluetooth chipset AR3012

File: ar3k/AthrBT_0x01020001.dfu
File: ar3k/ramps_0x01020001_26.dfu
File: ar3k/AthrBT_0x01020200.dfu
File: ar3k/ramps_0x01020200_26.dfu
File: ar3k/ramps_0x01020200_40.dfu
File: ar3k/AthrBT_0x31010000.dfu
File: ar3k/ramps_0x31010000_40.dfu
File: ar3k/AthrBT_0x11020000.dfu
File: ar3k/ramps_0x11020000_40.dfu
File: ar3k/ramps_0x01020201_26.dfu
File: ar3k/ramps_0x01020201_40.dfu
File: ar3k/AthrBT_0x41020000.dfu
File: ar3k/ramps_0x41020000_40.dfu
File: ar3k/AthrBT_0x11020100.dfu
File: ar3k/ramps_0x11020100_40.dfu

File: ar3k/AthrBT_0x31010100.dfu
File: ar3k/ramps_0x31010100_40.dfu

Licence: Redistributable. See LICENCE.atheros_firmware for details

Driver: DFU Driver for Atheros bluetooth chipset AR3012

File: ar3k/AthrBT_0x01020201.dfu
File: ar3k/1020201coex/ramps_0x01020201_26_HighPriority.dfu

Licence: Redistributable. See LICENSE.QualcommAtheros_ar3k for details

Driver:Atheros AR300x UART HCI Bluetooth Chip driver

File: ar3k/1020201/PS_ASIC.pst
File: ar3k/1020201/RamPatch.txt
File: ar3k/1020200/ar3kbdaddr.pst
File: ar3k/1020200/PS_ASIC.pst
File: ar3k/1020200/RamPatch.txt
File: ar3k/30101/ar3kbdaddr.pst
File: ar3k/30101/PS_ASIC.pst
File: ar3k/30101/RamPatch.txt
File: ar3k/30000/ar3kbdaddr.pst
File: ar3k/30000/PS_ASIC.pst
File: ar3k/30000/RamPatch.txt

Licence: Redistributable. See LICENCE.atheros_firmware for details

Driver: ath6kl - Atheros support for AR6003

File: ath6k/AR6004/hw1.3/fw-3.bin
File: ath6k/AR6004/hw1.3/bdata.bin
File: ath6k/AR6004/hw1.2/fw-2.bin
File: ath6k/AR6004/hw1.2/bdata.bin
File: ath6k/AR6003/hw1.0/otp.bin.z77
File: ath6k/AR6003/hw1.0/bdata.SD31.bin
File: ath6k/AR6003/hw1.0/bdata.SD32.bin
File: ath6k/AR6003/hw1.0/data.patch.bin
File: ath6k/AR6003/hw1.0/bdata.WB31.bin
File: ath6k/AR6003/hw1.0/athwlan.bin.z77
File: ath6k/AR6003/hw2.1.1/fw-2.bin
File: ath6k/AR6003/hw2.1.1/fw-3.bin
File: ath6k/AR6003/hw2.1.1/otp.bin
File: ath6k/AR6003/hw2.1.1/athwlan.bin
File: ath6k/AR6003/hw2.1.1/endpointping.bin
File: ath6k/AR6003/hw2.1.1/bdata.SD31.bin
File: ath6k/AR6003/hw2.1.1/bdata.SD32.bin
File: ath6k/AR6003/hw2.1.1/data.patch.bin
File: ath6k/AR6003/hw2.1.1/bdata.WB31.bin
File: ath6k/AR6003/hw2.0/otp.bin.z77
File: ath6k/AR6003/hw2.0/bdata.SD31.bin
File: ath6k/AR6003/hw2.0/bdata.SD32.bin
File: ath6k/AR6003/hw2.0/data.patch.bin
File: ath6k/AR6003/hw2.0/bdata.WB31.bin

File: ath6k/AR6003/hw2.0/athwlan.bin.z77
File: ath6k/AR6002/eeprom.data
File: ath6k/AR6002/eeprom.bin
File: ath6k/AR6002/athwlan.bin.z77
File: ath6k/AR6002/data.patch.hw2_0.bin

Licence: Redistributable. See LICENCE.atheros_firmware for details

Driver: ath10k - Qualcomm Atheros support for QCA988x family of chips

File: ath10k/QCA988X/hw2.0/board.bin
File: ath10k/QCA988X/hw2.0/firmware-4.bin
Version: 10.2.4.45
Licence: ath10k/QCA988X/hw2.0/notice_ath10k_firmware-4.txt
File: ath10k/QCA988X/hw2.0/firmware-5.bin
Version: 10.2.4.70.54
Licence: ath10k/QCA988X/hw2.0/notice_ath10k_firmware-5.txt
File: ath10k/QCA6174/hw2.1/board.bin
File: ath10k/QCA6174/hw2.1/board-2.bin
File: ath10k/QCA6174/hw2.1/firmware-5.bin
Version: SW_RM.1.1.1-00157-QCARMSWPZ-1
Licence: ath10k/QCA6174/hw2.1/notice_ath10k_firmware-5.txt
File: ath10k/QCA6174/hw3.0/board.bin
File: ath10k/QCA6174/hw3.0/board-2.bin
File: ath10k/QCA6174/hw3.0/firmware-4.bin
Version: WLAN.RM.2.0-00180-QCARMSWPZ-1
Licence: ath10k/QCA6174/hw3.0/notice_ath10k_firmware-4.txt
File: ath10k/QCA9377/hw1.0/board.bin
File: ath10k/QCA9377/hw1.0/board-2.bin
File: ath10k/QCA9377/hw1.0/firmware-5.bin
Version: WLAN.TF.1.0-00267-1
Licence: ath10k/QCA9377/hw1.0/notice_ath10k_firmware-5.txt
File: ath10k/QCA99X0/hw2.0/board.bin
File: ath10k/QCA99X0/hw2.0/firmware-5.bin
Version: 10.4.1.00030-1
Licence: ath10k/QCA99X0/hw2.0/notice_ath10k_firmware-5.txt
File: ath10k/QCA4019/hw1.0/board-2.bin
File: ath10k/QCA4019/hw1.0/firmware-5.bin
Version: 10.4-3.2.1-00028
Licence: ath10k/QCA4019/hw1.0/notice_ath10k_firmware-5.txt
File: ath10k/QCA9887/hw1.0/board.bin
File: ath10k/QCA9887/hw1.0/firmware-5.bin
Version: 10.2.4-1.0-00013
Licence: ath10k/QCA9887/hw1.0/notice_ath10k_firmware-5.txt
File: ath10k/QCA9888/hw2.0/board-2.bin
File: ath10k/QCA9888/hw2.0/firmware-5.bin
Version: 10.4-3.2-00072
Licence: ath10k/QCA9888/hw2.0/notice_ath10k_firmware-5.txt
File: ath10k/QCA9984/hw1.0/board-2.bin
File: ath10k/QCA9984/hw1.0/firmware-5.bin
Version: 10.4-3.2-00072
Licence: ath10k/QCA9984/hw1.0/notice_ath10k_firmware-5.txt

Licence: Redistributable. See LICENSE.QualcommAtheros_ath10k for details

Driver: myri10ge - Myri10GE 10GbE NIC driver

File: myri10ge_eth_z8e.dat
File: myri10ge_ethp_z8e.dat
File: myri10ge_rss_eth_z8e.dat
File: myri10ge_rss_ethp_z8e.dat
File: myri10ge_eth_big_z8e.dat
File: myri10ge_ethp_big_z8e.dat
File: myri10ge_rss_eth_big_z8e.dat
File: myri10ge_rss_ethp_big_z8e.dat
Version: 1.4.57

License: Redistributable. See LICENCE.myri10ge_firmware for details.

Driver: ath6kl - Atheros support for AR6003 WiFi-Bluetooth combo module

File: ath6k/AR6003.1/hw2.1.1/athwlan.bin
File: ath6k/AR6003.1/hw2.1.1/bdata.SD31.bin
File: ath6k/AR6003.1/hw2.1.1/bdata.SD32.bin
File: ath6k/AR6003.1/hw2.1.1/bdata.WB31.bin
File: ath6k/AR6003.1/hw2.1.1/data.patch.bin
File: ath6k/AR6003.1/hw2.1.1/endpointping.bin
File: ath6k/AR6003.1/hw2.1.1/otp.bin

License: Redistributable. See LICENCE.atheros_firmware for details

Driver: ath6kl - Atheros support for AR3001 WiFi-Bluetooth combo module

File: ar3k/30101coex/ar3kbdaddr.pst
File: ar3k/30101coex/PS_ASIC_aclLowPri.pst
File: ar3k/30101coex/PS_ASIC_aclHighPri.pst
File: ar3k/30101coex/PS_ASIC.pst
File: ar3k/30101coex/RamPatch.txt

License: Redistributable. See LICENCE.atheros_firmware for details

Driver: drxk - Micronas DRX-K demodulator driver

File: dvb-usb-terratec-h5-drxk.fw

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Driver: ene-ub6250 -- ENE UB6250 SD card reader driver

File: ene-ub6250/sd_init1.bin
File: ene-ub6250/sd_init2.bin
File: ene-ub6250/sd_rdwr.bin
File: ene-ub6250/ms_init.bin
File: ene-ub6250/msp_rdwr.bin
File: ene-ub6250/ms_rdwr.bin

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Driver: isci -- Intel C600 SAS controller driver

File: isci/isci_firmware.bin
Source: isci/

Licence: GPLv2. See GPL-2 for details.

Driver: ar5523 -- Atheros AR5523 based USB Wifi dongles

File: ar5523.bin

Licence: Redistributable. See LICENCE.atheros_firmware for details

Driver: s5p-mfc - Samsung MFC video encoder/decoder driver

File: s5p-mfc.fw
File: s5p-mfc-v6.fw
File: s5p-mfc-v6-v2.fw
File: s5p-mfc-v7.fw
File: s5p-mfc-v8.fw

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Driver: carl9170 -- Atheros AR9170 802.11 draft-n USB driver

File: carl9170-1.fw
Version: 1.9.6
Source: carl9170fw/

Downloaded from <http://linuxwireless.org/en/users/Drivers/carl9170>

Licence: GPLv2. Some build scripts use the New BSD (3-clause) licence.. See GPL-2 for details.

Driver: snd-hda-codec-ca0132 - Creative Sound Core3D codec

File: ctefx.bin
File: ctspeq.bin

Licence: Redistributable. See LICENCE.ca0132 for details

Found also in alsa-firmware package.

Driver: btusb - Bluetooth USB driver

File: intel/ibt-hw-37.7.bseq
Version: 1316.02.00
File: intel/ibt-hw-37.7.10-fw-1.80.2.3.d.bseq
Version: BT_WilkinsPeak_B3_REL_86_0001
File: intel/ibt-hw-37.7.10-fw-1.0.2.3.d.bseq
Version: BT_WilkinsPeak_B3_REL_86_0001
File: intel/ibt-hw-37.7.10-fw-1.80.1.2d.d.bseq
Version: BT_WilkinsPeak_B5_REL_39_0001
File: intel/ibt-hw-37.7.10-fw-1.0.1.2d.d.bseq
Version: BT_WilkinsPeak_B5_REL_39_0001
File: intel/ibt-hw-37.8.bseq
Version: 1339_02.00
File: intel/ibt-hw-37.8.10-fw-1.10.2.27.d.bseq
Version: BT_StonePeak_C0_REL_59_0001
File: intel/ibt-hw-37.8.10-fw-1.10.3.11.e.bseq
Version: BT_StonePeak_D0_REL_37_0002
File: intel/ibt-hw-37.8.10-fw-22.50.19.14.f.bseq
Version: BT_StonePeak_D1_REL_28_0001
File: intel/ibt-11-5.ddc
Version: LnP/SfP_REL0370
File: intel/ibt-11-5.sfi
Version: BT_LightningPeak_REL0370
File: intel/ibt-12-16.ddc
Version: BT_WindStormPeak_REL0101
File: intel/ibt-12-16.sfi
Version: BT_WindStormPeak_REL0101

Licence: Redistributable. See LICENCE.ibt_firmware for details

File: rtl_bt/rtl8192ee_fw.bin
File: rtl_bt/rtl8192eu_fw.bin
File: rtl_bt/rtl8723a_fw.bin
File: rtl_bt/rtl8723b_fw.bin
File: rtl_bt/rtl8761a_fw.bin
File: rtl_bt/rtl8812ae_fw.bin
File: rtl_bt/rtl8821a_fw.bin
File: rtl_bt/rtl8822b_fw.bin
File: rtl_bt/rtl8822b_config.bin

Licence: Redistributable. See LICENCE.rtlwifi_firmware.txt for details.

Found in vendor driver, linux_bt_usb_2.11.20140423_8723be.rar
From https://github.com/troy-tan/driver_store
Files rtl_bt/rtl8822b_* came directly from Realtek. These files are updated on April 14, 2017.

Driver: btmtk_usb - Bluetooth USB driver

File: mt7650.bin

Licence: Redistributable. See LICENCE.ralink_a_mEDIATEK_company_firmware for details

Driver: rp2 -- Control RocketPort 2 serial driver

File: rp2.fw

Licence: Redistributable.

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Driver: go7007

File: go7007/s2250-1.fw

File: go7007/s2250-2.fw

Link: s2250.fw -> go7007/s2250-2.fw

Link: s2250_loader.fw -> go7007/s2250-1.fw

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File: go7007/go7007fw.bin

File: go7007/go7007tv.bin

File: go7007/lr192.fw

File: go7007/px-m402u.fw

File: go7007/px-tv402u.fw

File: go7007/wis-startrek.fw

Licence: Redistributable. See LICENCE.go7007 for details

Driver: microcode_amd - AMD CPU Microcode Update Driver for Linux

File: amd-ucode/microcode_amd.bin

Version: 2013-07-10

File: amd-ucode/microcode_amd_fam15h.bin
Version: 2016-03-16
File: amd-ucode/microcode_amd_fam16h.bin
Version: 2014-10-28

License: Redistributable. See LICENSE.amd-ucode for details

Driver: mxu11x0 - MOXA UPort 11x0 USB Serial hub driver

File: moxa/moxa-1110.fw
File: moxa/moxa-1130.fw
File: moxa/moxa-1131.fw
File: moxa/moxa-1150.fw
File: moxa/moxa-1151.fw

License: Redistributable. See LICENCE.moxa for details

Driver: mxuport - MOXA UPort USB Serial hub driver

File: moxa/moxa-1250.fw
File: moxa/moxa-1251.fw
File: moxa/moxa-1410.fw
File: moxa/moxa-1450.fw
File: moxa/moxa-1451.fw
File: moxa/moxa-1613.fw
File: moxa/moxa-1618.fw
File: moxa/moxa-1653.fw
File: moxa/moxa-1658.fw

License: Redistributable. See LICENCE.moxa for details

Driver: cw1200 - ST-E CW1100/CW1200 WLAN driver

File: wsm_22.bin
Version: WSM395
Licence: Redistributable. See LICENCE.cw1200 for details.

File: sdd_sagrad_1091_1098.bin

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(Please note that the actual device firmware is separately licensed)

Driver: BFA/BNA - QLogic BR-series Adapter FC/FCOE drivers

File: cbfw-3.2.1.1.bin
File: ctfw-3.2.1.1.bin
File: ct2fw-3.2.1.1.bin
File: cbfw-3.2.3.0.bin
File: ctfw-3.2.3.0.bin
File: ct2fw-3.2.3.0.bin
File: cbfw-3.2.5.1.bin
File: ctfw-3.2.5.1.bin
File: ct2fw-3.2.5.1.bin

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Driver: qat - Intel(R) QAT crypto accelerator

File: qat_895xcc.bin
File: qat_895xcc_mmp.bin
File: qat_c3xxx.bin
File: qat_c3xxx_mmp.bin
File: qat_c62x.bin
File: qat_c62x_mmp.bin
Link: qat_mmp.bin -> qat_895xcc_mmp.bin

Licence: Redistributable. See LICENCE.qat_firmware for details

Driver: rsi -- Redpine Signals Inc 91x driver

File: rsi_91x.fw

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Driver: xhci-rcar -- Renesas R-Car Gen2/3 USB 3.0 host controller driver

File: r8a779x_usb3_v1.dlmem
File: r8a779x_usb3_v2.dlmem
File: r8a779x_usb3_v3.dlmem

Licence: Redistributable. See LICENCE.r8a779x_usb3 for details.

Driver: snd_soc_sst_acpi

File: intel/fw_sst_0f28.bin-48kHz_i2s_master

License: Redistributable. See LICENCE.fw_sst_0f28 for details

Driver: as102 - Abilis Systems Single DVB-T Receiver

File: as102_data1_st.hex
File: as102_data2_st.hex

License: Redistributable. See LICENCE.Abilis for details

Driver: it9135 -- ITEtech IT913x DVB-T USB driver

File: dvb-usb-it9135-01.fw
File: dvb-usb-it9135-02.fw

Licence: Redistributable. See LICENCE.it913x for details

Driver: snd_soc_sst_acpi

File: intel/IntcSST2.bin
Version: 8.4.1.77

License: Redistributable. See LICENCE.IntcSST2 for details

Driver: snd_intel_sst_core

File: intel/fw_sst_0f28.bin
File: intel/fw_sst_0f28_ssp0.bin

License: Redistributable. See LICENCE.fw_sst_0f28 for details

Driver: snd_intel_sst_core

File: intel/fw_sst_22a8.bin
Version: 01.0B.02.02

License: Redistributable. See LICENCE.fw_sst_0f28 for details

Driver: snd-soc-skl

File: intel/dsp_fw_release_v827.bin
Version: 8.20.00.927
File: intel/dsp_fw_release_v869.bin
Version: 8.20.00.869
File: intel/dsp_fw_release_v896.bin
Version: 8.20.00.896
File: intel/dsp_fw_release_v927.bin
Version: 8.20.00.927
File: intel/dsp_fw_release_v948.bin
Version: 8.20.00.948
File: intel/dsp_fw_release_v951.bin
Version: 8.20.00.951
File: intel/dsp_fw_release_v958.bin
Version: 8.20.00.958
File: intel/dsp_fw_release_v969.bin
Version: 8.20.00.969
Link: intel/dsp_fw_release.bin -> intel/dsp_fw_release_v969.bin

License: Redistributable. See LICENCE.adsp_sst for details

File: intel/dsp_fw_bxtv_v430.bin
Version: 9.00.00.430
File: intel/dsp_fw_bxtv_v702.bin
Version: 9.00.00.702
File: intel/dsp_fw_bxtv_v1118.bin
Version: 9.22.00.1118
File: intel/dsp_fw_bxtv_v2010.bin
Version: 9.22.01.2010_44
File: intel/dsp_fw_bxtv_v2219.bin
Version: 9.22.01.2219_64
Link: intel/dsp_fw_bxtv.bin -> intel/dsp_fw_bxtv_v2219.bin

License: Redistributable. See LICENCE.adsp_sst for details

File: intel/dsp_fw_kbl_v701.bin
Version: 9.21.00.701
File: intel/dsp_fw_kbl_v1037.bin
Version: 09.21.00.1037
Link: intel/dsp_fw_kbl.bin -> dsp_fw_kbl_v1037.bin

License: Redistributable. See LICENCE.adsp_sst for details

File: intel/dsp_fw_glk_v1814.bin
Version: 9.92.01.1814
Link: intel/dsp_fw_glk.bin -> dsp_fw_glk_v1814.bin

License: Redistributable. See LICENCE.adsp_sst for details

Driver: smsmdtv - Siano MDTV Core module

File: cmmb_vega_12mhz.inp

File: cmmmb_venice_12mhz.inp
File: dvb_nova_12mhz.inp
File: dvb_nova_12mhz_b0.inp
File: isdbt_nova_12mhz.inp
File: isdbt_nova_12mhz_b0.inp
File: isdbt_rio.inp
File: sms1xxx-hcw-55xxx-dvbt-02.fw
File: sms1xxx-hcw-55xxx-isdbt-02.fw
File: sms1xxx-nova-a-dvbt-01.fw
File: sms1xxx-nova-b-dvbt-01.fw
File: sms1xxx-stellar-dvbt-01.fw
File: tdmn_nova_12mhz.inp

Licence: Redistributable. See LICENCE.siano for details

Driver: xhci-tegra -- NVIDIA Tegra XHCI driver

File: nvidia/tegra124/xusb.bin
Version: v45.46

File: nvidia/tegra210/xusb.bin
Version: v50.10

Licence: Redistributable. See LICENCE.nvidia for details

Driver: atusb - ATUSB IEEE 802.15.4 transceiver driver

File: atusb/atusb-0.2.dfu
Version: 0.2
File: atusb/atusb-0.3.dfu
Version: 0.3
File: atusb/rzusb-0.3.bin
Version: 0.3
Info: atusb/ChangeLog

Licence: GPLv2 or later. See GPL-2 and GPL-3 for details.

Driver: qca - Qualcomm Atheros Bluetooth support for QCA61x4 chips

File: qca/nvm_usb_00000201.bin
File: qca/nvm_usb_00000200.bin
File: qca/nvm_usb_00000300.bin
File: qca/nvm_usb_00000302.bin
File: qca/nvm_00130300.bin
File: qca/nvm_00130302.bin
File: qca/rampatch_usb_00000200.bin
File: qca/rampatch_usb_00000201.bin
File: qca/rampatch_usb_00000300.bin
File: qca/rampatch_usb_00000302.bin
File: qca/rampatch_00130300.bin
File: qca/rampatch_00130302.bin

Licence: Redistributable. See LICENSE.QualcommAtheros_ath10k and qca/NOTICE.txt for details

Driver: liquidio -- Cavium LiquidIO driver

File: liquidio/lio_23xx_nic.bin
Version: v1.5.1

File: liquidio/lio_210nv_nic.bin
Version: v1.5.1

File: liquidio/lio_210sv_nic.bin
Version: v1.5.1

File: liquidio/lio_410nv_nic.bin
Version: v1.5.1

Licence: Redistributable. See LICENCE.cavium for details

Driver: i915 -- Intel Integrated Graphics driver

File: i915/skl_dmc_ver1_23.bin
File: i915/skl_dmc_ver1_26.bin
File: i915/skl_dmc_ver1.bin
Version: DMC API/ABI ver 1 - release 26 for Skylake

File: i915/bxt_dmc_ver1_07.bin
File: i915/bxt_dmc_ver1.bin
Version: DMC API/ABI ver 1 - release 07 for Broxton

File: i915/kbl_dmc_ver1_01.bin
File: i915/kbl_dmc_ver1.bin
Version: DMC API/ABI ver 1 - release 01 for Kabylake

File: i915/skl_guc_ver1.bin
Version: Guc API/ABI ver 1 - release 1059 for Skylake

File: i915/skl_guc_ver4.bin
Version: Guc API/ABI ver 4 - release 3 for Skylake

File: i915/skl_guc_ver6_1.bin
File: i915/skl_guc_ver6.bin
Version: Guc API/ABI ver 6 - release 1 for Skylake

File: i915/skl_huc_ver01_07_1398.bin
Version: HuC API/ABI ver 1 - release 7 for Skylake

File: i915/bxt_huc_ver01_07_1398.bin
Version: HuC API/APB ver 1 - release 7 for Broxton

File: i915/kbl_huc_ver02_00_1810.bin
Version: HuC API/APB ver 2 - for Kabylake

File: i915/bxt_guc_ver8_7.bin
Version: Guc API/APB ver 8 - release 7 for Broxton

File: i915/kbl_guc_ver9_14.bin
Version: Guc API/APB ver 9 release 14 for Kabylake

License: Redistributable. See LICENSE.i915 for details

Driver: nouveau - NVIDIA GPU driver

File: nvidia/gk20a/fecs_data.bin
File: nvidia/gk20a/fecs_inst.bin
File: nvidia/gk20a/gpccs_data.bin
File: nvidia/gk20a/gpccs_inst.bin
File: nvidia/gk20a/sw_bundle_init.bin
File: nvidia/gk20a/sw_ctx.bin
File: nvidia/gk20a/sw_method_init.bin
File: nvidia/gk20a/sw_nonctx.bin
File: nvidia/gm200/acr/bl.bin
File: nvidia/gm200/acr/ucode_load.bin
File: nvidia/gm200/acr/ucode_unload.bin
File: nvidia/gm200/gr/fecs_bl.bin
File: nvidia/gm200/gr/fecs_data.bin
File: nvidia/gm200/gr/fecs_inst.bin
File: nvidia/gm200/gr/fecs_sig.bin
File: nvidia/gm200/gr/gpccs_bl.bin
File: nvidia/gm200/gr/gpccs_data.bin
File: nvidia/gm200/gr/gpccs_inst.bin
File: nvidia/gm200/gr/gpccs_sig.bin
File: nvidia/gm200/gr/sw_bundle_init.bin
File: nvidia/gm200/gr/sw_ctx.bin
File: nvidia/gm200/gr/sw_method_init.bin
File: nvidia/gm200/gr/sw_nonctx.bin
File: nvidia/gm204/acr/bl.bin
File: nvidia/gm204/acr/ucode_load.bin
File: nvidia/gm204/acr/ucode_unload.bin
File: nvidia/gm204/gr/fecs_bl.bin
File: nvidia/gm204/gr/fecs_data.bin
File: nvidia/gm204/gr/fecs_inst.bin
File: nvidia/gm204/gr/fecs_sig.bin
File: nvidia/gm204/gr/gpccs_bl.bin
File: nvidia/gm204/gr/gpccs_data.bin
File: nvidia/gm204/gr/gpccs_inst.bin
File: nvidia/gm204/gr/gpccs_sig.bin
File: nvidia/gm204/gr/sw_bundle_init.bin
File: nvidia/gm204/gr/sw_ctx.bin
File: nvidia/gm204/gr/sw_method_init.bin
File: nvidia/gm204/gr/sw_nonctx.bin
File: nvidia/gm206/acr/bl.bin
File: nvidia/gm206/acr/ucode_load.bin
File: nvidia/gm206/acr/ucode_unload.bin
File: nvidia/gm206/gr/fecs_bl.bin
File: nvidia/gm206/gr/fecs_data.bin
File: nvidia/gm206/gr/fecs_inst.bin
File: nvidia/gm206/gr/fecs_sig.bin
File: nvidia/gm206/gr/gpccs_bl.bin
File: nvidia/gm206/gr/gpccs_data.bin
File: nvidia/gm206/gr/gpccs_inst.bin
File: nvidia/gm206/gr/gpccs_sig.bin
File: nvidia/gm206/gr/sw_bundle_init.bin
File: nvidia/gm206/gr/sw_ctx.bin
File: nvidia/gm206/gr/sw_method_init.bin
File: nvidia/gm206/gr/sw_nonctx.bin
File: nvidia/gm20b/acr/bl.bin

File: nvidia/gm20b/acr/ucode_load.bin
File: nvidia/gm20b/gr/fecs_bl.bin
File: nvidia/gm20b/gr/fecs_data.bin
File: nvidia/gm20b/gr/fecs_inst.bin
File: nvidia/gm20b/gr/fecs_sig.bin
File: nvidia/gm20b/gr/gpccs_data.bin
File: nvidia/gm20b/gr/gpccs_inst.bin
File: nvidia/gm20b/gr/sw_bundle_init.bin
File: nvidia/gm20b/gr/sw_ctx.bin
File: nvidia/gm20b/gr/sw_method_init.bin
File: nvidia/gm20b/gr/sw_nonctx.bin
File: nvidia/gm20b/pmu/desc.bin
File: nvidia/gm20b/pmu/image.bin
File: nvidia/gm20b/pmu/sig.bin
File: nvidia/gp100/acr/bl.bin
File: nvidia/gp100/acr/ucode_load.bin
File: nvidia/gp100/acr/ucode_unload.bin
File: nvidia/gp100/gr/fecs_bl.bin
File: nvidia/gp100/gr/fecs_data.bin
File: nvidia/gp100/gr/fecs_inst.bin
File: nvidia/gp100/gr/fecs_sig.bin
File: nvidia/gp100/gr/gpccs_bl.bin
File: nvidia/gp100/gr/gpccs_data.bin
File: nvidia/gp100/gr/gpccs_inst.bin
File: nvidia/gp100/gr/gpccs_sig.bin
File: nvidia/gp100/gr/sw_bundle_init.bin
File: nvidia/gp100/gr/sw_ctx.bin
File: nvidia/gp100/gr/sw_method_init.bin
File: nvidia/gp100/gr/sw_nonctx.bin
File: nvidia/gp102/acr/bl.bin
File: nvidia/gp102/acr/ucode_load.bin
File: nvidia/gp102/acr/ucode_unload.bin
File: nvidia/gp102/acr/unload_bl.bin
File: nvidia/gp102/gr/fecs_bl.bin
File: nvidia/gp102/gr/fecs_data.bin
File: nvidia/gp102/gr/fecs_inst.bin
File: nvidia/gp102/gr/fecs_sig.bin
File: nvidia/gp102/gr/gpccs_bl.bin
File: nvidia/gp102/gr/gpccs_data.bin
File: nvidia/gp102/gr/gpccs_inst.bin
File: nvidia/gp102/gr/gpccs_sig.bin
File: nvidia/gp102/gr/sw_bundle_init.bin
File: nvidia/gp102/gr/sw_ctx.bin
File: nvidia/gp102/gr/sw_method_init.bin
File: nvidia/gp102/gr/sw_nonctx.bin
File: nvidia/gp102/nvdec/scrubber.bin
File: nvidia/gp102/sec2/desc.bin
File: nvidia/gp102/sec2/image.bin
File: nvidia/gp102/sec2/sig.bin
File: nvidia/gp104/acr/bl.bin
File: nvidia/gp104/acr/ucode_load.bin
File: nvidia/gp104/acr/ucode_unload.bin
File: nvidia/gp104/acr/unload_bl.bin
File: nvidia/gp104/gr/fecs_bl.bin
File: nvidia/gp104/gr/fecs_data.bin
File: nvidia/gp104/gr/fecs_inst.bin
File: nvidia/gp104/gr/fecs_sig.bin
File: nvidia/gp104/gr/gpccs_bl.bin
File: nvidia/gp104/gr/gpccs_data.bin
File: nvidia/gp104/gr/gpccs_inst.bin

File: nvidia/gp104/gr/gpccs_sig.bin
File: nvidia/gp104/gr/sw_bundle_init.bin
File: nvidia/gp104/gr/sw_ctx.bin
File: nvidia/gp104/gr/sw_method_init.bin
File: nvidia/gp104/gr/sw_nonctx.bin
File: nvidia/gp104/nvdec/scrubber.bin
File: nvidia/gp104/sec2/desc.bin
File: nvidia/gp104/sec2/image.bin
File: nvidia/gp104/sec2/sig.bin
File: nvidia/gp106/acr/bl.bin
File: nvidia/gp106/acr/ucode_load.bin
File: nvidia/gp106/acr/ucode_unload.bin
File: nvidia/gp106/acr/unload_bl.bin
File: nvidia/gp106/gr/fecs_bl.bin
File: nvidia/gp106/gr/fecs_data.bin
File: nvidia/gp106/gr/fecs_inst.bin
File: nvidia/gp106/gr/fecs_sig.bin
File: nvidia/gp106/gr/gpccs_bl.bin
File: nvidia/gp106/gr/gpccs_data.bin
File: nvidia/gp106/gr/gpccs_inst.bin
File: nvidia/gp106/gr/gpccs_sig.bin
File: nvidia/gp106/gr/sw_bundle_init.bin
File: nvidia/gp106/gr/sw_ctx.bin
File: nvidia/gp106/gr/sw_method_init.bin
File: nvidia/gp106/gr/sw_nonctx.bin
File: nvidia/gp106/nvdec/scrubber.bin
File: nvidia/gp106/sec2/desc.bin
File: nvidia/gp106/sec2/image.bin
File: nvidia/gp106/sec2/sig.bin
File: nvidia/gp107/acr/bl.bin
File: nvidia/gp107/acr/ucode_load.bin
File: nvidia/gp107/acr/ucode_unload.bin
File: nvidia/gp107/acr/unload_bl.bin
File: nvidia/gp107/gr/fecs_bl.bin
File: nvidia/gp107/gr/fecs_data.bin
File: nvidia/gp107/gr/fecs_inst.bin
File: nvidia/gp107/gr/fecs_sig.bin
File: nvidia/gp107/gr/gpccs_bl.bin
File: nvidia/gp107/gr/gpccs_data.bin
File: nvidia/gp107/gr/gpccs_inst.bin
File: nvidia/gp107/gr/gpccs_sig.bin
File: nvidia/gp107/gr/sw_bundle_init.bin
File: nvidia/gp107/gr/sw_ctx.bin
File: nvidia/gp107/gr/sw_method_init.bin
File: nvidia/gp107/gr/sw_nonctx.bin
File: nvidia/gp107/nvdec/scrubber.bin
File: nvidia/gp107/sec2/desc.bin
File: nvidia/gp107/sec2/image.bin
File: nvidia/gp107/sec2/sig.bin
File: nvidia/gp10b/acr/bl.bin
File: nvidia/gp10b/acr/ucode_load.bin
File: nvidia/gp10b/gr/fecs_bl.bin
File: nvidia/gp10b/gr/fecs_data.bin
File: nvidia/gp10b/gr/fecs_inst.bin
File: nvidia/gp10b/gr/fecs_sig.bin
File: nvidia/gp10b/gr/gpccs_bl.bin
File: nvidia/gp10b/gr/gpccs_data.bin
File: nvidia/gp10b/gr/gpccs_inst.bin
File: nvidia/gp10b/gr/gpccs_sig.bin
File: nvidia/gp10b/gr/sw_bundle_init.bin

File: nvidia/gp10b/gr/sw_ctx.bin
File: nvidia/gp10b/gr/sw_method_init.bin
File: nvidia/gp10b/gr/sw_nonctx.bin
File: nvidia/gp10b/pmu/desc.bin
File: nvidia/gp10b/pmu/image.bin
File: nvidia/gp10b/pmu/sig.bin

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Driver: wilc1000 - Atmel 802.11n WLAN driver for WILC1000

File: atmel/wilc1000_fw.bin
File: atmel/wilc1000_ap_fw.bin
File: atmel/wilc1000_p2p_fw.bin

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Driver: hfi1 - Intel OPA Gen 1 adapter

File: hfi1_dc8051.fw
Version: 0.47
File: hfi1_fabric.fw
Version: 0x1055
File: hfi1_pcie.fw
Version: 0x4755
File: hfi1_sbus.fw
Version: 0x10130001
File: hfi1_platform.dat

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Driver: knav_qmss_queue - TI Keystone 2 QMSS driver

File: ti-keystone/ks2_qmss_pdsp_acc48_k2_le_1_0_0_9.bin

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Driver: mwlfwifi - Marvell mac80211 driver for 80211ac cards.

File: mwlfwifi/88W8864.bin
Version: 7.2.8.6

File: mwlfwifi/88W8897.bin
Version: 8.2.0.10

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Driver: mtk-vpu - Mediatek VPU video processing unit driver

File: vpu_d.bin
File: vpu_p.bin

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Driver: rk3399-dptx - ROCKCHIP rk3399 dptx firmware

File: rockchip/dptx.bin
Version: 3.1

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Driver: mt76x2e - MediaTek MT76x2 Wireless MACs

File: mt7662.bin
Version: 1.9

File: mt7662_rom_patch.bin
Version: 0.0.2_P69

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Driver: nfp - Netronome Flow Processor

File: netronome/nic_AMD0081-0001_1x40.nffw
File: netronome/nic_AMD0097-0001_2x40.nffw
File: netronome/nic_AMD0099-0001_2x10.nffw
File: netronome/nic_AMD0081-0001_4x10.nffw
File: netronome/nic_AMD0097-0001_4x10_1x40.nffw
File: netronome/nic_AMD0099-0001_2x25.nffw
File: netronome/nic_AMD0096-0001_2x10.nffw
File: netronome/nic_AMD0097-0001_8x10.nffw

Version: v0.6.1

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1.

```
/*
* FILE:    sha2.c
* AUTHOR:  Aaron D. Gifford <me@aarongifford.com>
*
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* SUCH DAMAGE.
*
* $Id: sha2.c,v 1.1 2001/11/08 00:01:51 adg Exp adg $
*/
File: LLM_sp_sha2.c

/*
* FILE:    sha2.h
* AUTHOR:  Aaron D. Gifford <me@aarongifford.com>
*
```

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*

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*

* \$Id: sha2.h,v 1.1 2001/11/08 00:02:01 adg Exp adg \$

*/

File: LLM_sp_sha2.h

2.

/* utility to create the register check tables

* this includes inlined list.h safe for userspace.

*

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*

* Authors:

* Jerome Glisse

* Dave Airlie

*/

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*/

File: comm_lib.h

3.

/* crypto/aes/aes.h -*- mode:C; c-file-style: "eay" -*- */

/*

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* =====
*
*/

File Name list: aes.h, aes_locl.h

4.

/* crypto/aes/aes_core.c -*- mode:C; c-file-style: "eay" -*- */
/**

* rijndael-alg-fst.c

*

* @version 3.0 (December 2000)

*

* Optimised ANSI C code for the Rijndael cipher (now AES)

*

* @author Vincent Rijmen <vincent.rijmen@esat.kuleuven.ac.be>

* @author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>

* @author Paulo Barreto <paulo.barreto@terra.com.br>

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*/

File: aes_core.c

5.

/* =====

EDIT HISTORY FOR FILE

\$Header: //depot/asic/msmshared/sec/sec/MSM_SEC.03.03.08.xx/aes.h#1 \$

\$DateTime: 2006/12/11 00:43:21 \$ \$Author: davidf \$

when	who	what, where, why
11/12/04	rv	changes to correct LINT errors
02/02/04	rwh	Small mods to open source.

====*/

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Dr B. R. Gladman <brg@gladman.uk.net> 1st June 2001.

This is an implementation of the AES encryption algorithm (Rijndael) designed by Joan Daemen and Vincent Rijmen. This version is designed to provide both fixed and dynamic block and key lengths and can also run with either big or little endian internal byte order.

NOTE: Input block and key lengths are given in terms of the lengths of the byte arrays involved, the legal values being 16, 24 and 32.

A. THE CIPHER INTERFACE

byte	(an unsigned 8-bit type)
word	(an unsigned 32-bit type)
aes_ret:	(a signed 16 bit type for function return values)
aes_good	(value != 0, a good return)
aes_bad	(value == 0, an error return)
enum aes_key:	(encryption direction)
enc	(set key for encryption)
dec	(set key for decryption)
both	(set key for both)
class or struct aes	(structure for context)

C subroutine calls:

```
aes_ret set_blk(const word block_length, aes *cx) (variable block size)
aes_ret set_key(const byte key[], const word key_length,
               const enum aes_key direction, aes *cx)
aes_ret encrypt(const byte input_blk[], byte output_blk[], const aes *cx)
aes_ret decrypt(const byte input_blk[], byte output_blk[], const aes *cx)
```

IMPORTANT NOTE: If you are using this C interface and your compiler does not set the memory used for objects to zero before use, you will need to ensure that cx.mode is set to zero before using the C subroutine calls.

C++ aes class subroutines:

```
aes_ret set_blk(const word block_length) (variable block size)
aes_ret set_key(const byte key[], const word key_length,
               const aes_key direction)
aes_ret encrypt(const byte input_blk[], byte output_blk[]) const
aes_ret decrypt(const byte input_blk[], byte output_blk[]) const
```

The block length inputs to set_block and set_key are in numbers of BYTES, not bits. The calls to subroutines must be made in the above order but multiple calls can be made without repeating earlier calls if their parameters have not changed. If the cipher block length is variable but set_blk has not been called before cipher operations a value of 16 is assumed (that is, the AES block size). In contrast to earlier versions the block and key length parameters are now checked for correctness and the encryption and decryption routines check to ensure that an appropriate key has been set before they are called.

B. BYTE ORDER WITHIN 32 BIT WORDS

The fundamental data processing units in Rijndael are 8-bit bytes. The

input, the output and the key input are all enumerated arrays of bytes in which bytes are numbered starting at zero and increasing to one less than the number of bytes in the array in question. When these inputs and outputs are considered as bit sequences, the n'th byte contains bits $8n$ to $8n+7$ of the sequence with the lower numbered bit mapped to the most significant bit within the byte (i.e. that having a numeric value of 128). However, Rijndael can be implemented more efficiently using 32-bit words to process 4 bytes at a time provided that the order of bytes within words is known. This order is called big-endian if the lowest numbered bytes in words have the highest numeric significance and little-endian if the opposite applies. This code can work in either order irrespective of the native order of the machine on which it runs. The byte order used internally is set by defining `INTERNAL_BYTE_ORDER` whereas the order for all inputs and outputs is specified by defining `EXTERNAL_BYTE_ORDER`, the only purpose of the latter being to determine if a byte order change is needed immediately after input and immediately before output to account for the use of a different internal byte order. In almost all situations both of these defines will be set to the native order of the processor on which the code is to run but other settings may sometimes be useful in special circumstances.

```
#define INTERNAL_BYTE_ORDER LITTLE_ENDIAN
#define EXTERNAL_BYTE_ORDER LITTLE_ENDIAN
```

C. COMPILATION

To compile AES (Rijndael) for use in C code

- a. Exclude the `AES_DLL` define in `aes.h`
- b. Exclude the `AES_IN_CPP` define in `aes.h`

To compile AES (Rijndael) for use in C++ code

- a. Exclude the `AES_DLL` define in `aes.h`
- b. Include the `AES_IN_CPP` define in `aes.h`

To compile AES (Rijndael) in C as a Dynamic Link Library

- a. Include the `AES_DLL` define in `aes.h`
- b. Compile the DLL. If using the test files, exclude `aes.c` from the test build project and compile it with the same defines as used for the DLL (ensure that the DLL path is correct)

D. CONFIGURATION OPTIONS (see also `aes.c`)

1. define `BLOCK_SIZE` to set the cipher block size (16, 24 or 32) or leave this undefined for dynamically variable block size (this will result in much slower code).
2. set `AES_IN_CPP` to use the code from C++ rather than C
3. set `AES_DLL` if AES (Rijndael) is to be compiled to a DLL
4. set `INTERNAL_BYTE_ORDER` to one of the above constants to set the internal byte order (the order used within the algorithm code)
5. set `EXTERNAL_BYTE_ORDER` to one of the above constants to set the byte order used at the external interfaces for the input, output and key byte arrays.

IMPORTANT NOTE: `BLOCK_SIZE` is in BYTES: 16, 24, 32 or undefined for `aes.c` and 16, 20, 24, 28, 32 or undefined for `aes++.c`. If left undefined a slower version providing variable block length is compiled

```
#define BLOCK_SIZE 16
```

Define `AES_IN_CPP` if you intend to use the AES C++ class rather than the

```

C code directly.

#define AES_IN_CPP

    Define AES_DLL if you wish to compile the code to produce a Windows DLL

#define AES_DLL

*/
File: aes.h

/*=====

                                EDIT HISTORY FOR FILE

$Header: //depot/asic/msmshared/sec/sec/MSM_SEC.03.03.08.xx/aes_tab.h#1 $
$DateTime: 2006/12/11 00:43:21 $ $Author: davidf $

when      who      what, where, why
-----
11/12/04  rv          changes to correct LINT errors
02/02/04  rwh        Small mods to open source.

=====*/

/*lint -e146 -e303 */
/* 146: Assuming a binary constant */
/* 303: String too long (try +macros) */

*/
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    Dr B. R. Gladman <brg@gladman.uk.net> 1st June 2001.
*/
File: aes_tab.h

=====

6.

=====
#FILE:Conftest.py
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#
#
# The purpose of this module is to define how a check is to be performed.
# Use one of the Check...() functions below.
#
#
# A context class is used that defines functions for carrying out the tests,
# logging and messages. The following methods and members must be present:
#
# context.Display(msg) Function called to print messages that are normally
#                       displayed for the user. Newlines are explicitly used.
#                       The text should also be written to the logfile!
#
# context.Log(msg)      Function called to write to a log file.
#
# context.BuildProg(text, ext)
#                       Function called to build a program, using "ext" for the
#                       file extension. Must return an empty string for
#                       success, an error message for failure.
#                       For reliable test results building should be done just
#                       like an actual program would be build, using the same
#                       command and arguments (including configure results so
#                       far).
#
# context.CompileProg(text, ext)
#                       Function called to compile a program, using "ext" for
#                       the file extension. Must return an empty string for
#                       success, an error message for failure.
#                       For reliable test results compiling should be done just
#                       like an actual source file would be compiled, using the
#                       same command and arguments (including configure results
#                       so far).
#
# context.AppendLIBS(lib_name_list)
#                       Append "lib_name_list" to the value of LIBS.
#                       "lib_namelist" is a list of strings.
#                       Return the value of LIBS before changing it (any type
#                       can be used, it is passed to SetLIBS() later.)
#
# context.PrependLIBS(lib_name_list)
#                       Prepend "lib_name_list" to the value of LIBS.
#                       "lib_namelist" is a list of strings.
#                       Return the value of LIBS before changing it (any type
#                       can be used, it is passed to SetLIBS() later.)
#
# context.SetLIBS(value)
#                       Set LIBS to "value". The type of "value" is what
#                       AppendLIBS() returned.
#                       Return the value of LIBS before changing it (any type
#                       can be used, it is passed to SetLIBS() later.)
#
# context.headerfilename

```

```

#           Name of file to append configure results to, usually
#           "confdefs.h".
#           The file must not exist or be empty when starting.
#           Empty or None to skip this (some tests will not work!).
#
# context.config_h (may be missing). If present, must be a string, which
#                 will be filled with the contents of a config_h file.
#
# context.vardict  Dictionary holding variables used for the tests and
#                 stores results from the tests, used for the build
#                 commands.
#                 Normally contains "CC", "LIBS", "CPPFLAGS", etc.
#
# context.havedict Dictionary holding results from the tests that are to
#                 be used inside a program.
#                 Names often start with "HAVE_". These are zero
#                 (feature not present) or one (feature present). Other
#                 variables may have any value, e.g., "PERLVERSION" can
#

```

```
=====
```

```
-----
```

```
=====
```

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```
=====
```

```
=====
```

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```

```
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* ===== */
```

```
=====  
/* =====  
* FILE: sha2.h  
* AUTHOR: Aaron D. Gifford - http://www.aarongifford.com/  
*  
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*  
* =====
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*/

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=====

=====

=====

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=====
=====
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*/

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=====
=====
=====
=====

```
/*
 * AES-based functions
 *
 *
 * - AES Key Wrap Algorithm (128-bit KEK) (RFC3394)
 * - One-Key CBC MAC (OMAC1) hash with AES-128
 * - AES-128 CTR mode encryption
 * - AES-128 EAX mode encryption/decryption
 * - AES-128 CBC
 *
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 *
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 */
```

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```
=====
=====

=====
=====
```

```
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 * Common helper macros, etc.
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 *
 */
```

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```
=====
=====

=====
=====
```

```
/*
 * SHA1 hash implementation and interface functions
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/*  
* $DateTime: 2013/07/24 11:35:54 $  
*/  
  
/* $OpenBSD: string.h,v 1.17 2006/01/06 18:53:04 millert Exp $ */  
/* $NetBSD: string.h,v 1.6 1994/10/26 00:56:30 cgd Exp $ */
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/*  
 * $DateTime: 2011/01/07 17:50:19 $  
*/  
  
/* $OpenBSD: strlcpy.c,v 1.11 2006/05/05 15:27:38 millert Exp $ */  
  
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// @(#)queue.h 8.5 (Berkeley) 8/20/94  
// $FreeBSD: src/sys/sys/queue.h,v 1.58 2004/04/07 04:19:49 imp Exp $  
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 *
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 *
 * Modifications to public domain implementation:
 * - support only 128-bit keys
 * - cleanup
 * - use C pre-processor to make it easier to change S table access
 * - added option (AES_SMALL_TABLES) for reducing code size by about 8 kB at
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```
/*
 * rijndael-alg-fst.c
 *
 * @version 3.0 (December 2000)
 *
 * Optimised ANSI C code for the Rijndael cipher (now AES)
 *
 * @author Vincent Rijmen <vincent.rijmen@esat.kuleuven.ac.be>
```

* @author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>
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 * This file defines the cryptographic functions that need to be implemented
 * for wpa_supplicant and hostapd. When TLS is not used, internal
 * implementation of MD5, SHA1, and AES is used and no external libraries are
 * required. When TLS is enabled (e.g., by enabling EAP-TLS or EAP-PEAP), the
 * crypto library used by the TLS implementation is expected to be used for
 * non-TLS needs, too, in order to save space by not implementing these
 * functions twice.
 *
 * Wrapper code for using each crypto library is in its own file (crypto*.c)
 * and one of these files is build and linked in to provide the functions
 * defined here.
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```
//
//
// This code implements the MD5 message-digest algorithm.
// The algorithm is due to Ron Rivest. This code was
// written by Colin Plumb in 1993, no copyright is claimed.
// This code is in the public domain; do with it what you wish.
//
// Equivalent code is available from RSA Data Security, Inc.
// This code has been tested against that, and is equivalent,
// except that you don't need to include two pages of legalese
// with every copy.
//
// To compute the message digest of a chunk of bytes, declare an
// MD5Context structure, pass it to MD5Init, call MD5Update as
// needed on buffers full of bytes, and then call MD5Final, which
// will fill a supplied 16-byte array with the digest.
//
```

```
/*=====
@file secrsa_encryption.c
```

This file provides RSA PKCS #1 v1.5 encryption/decryption implementation.

EDIT HISTORY FOR FILE

\$DateTime: 2013/08/06 21:06:51 \$

\$Author: pdhaval \$

when	who	what, where, why
------	-----	------------------

04/11/11	qxu	initial version
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```
//  
// Name:          lzuncompr.c  
// Author:        Vitaly Luban  
// Based on code from Marcus Geelnard  
// Description: LZ77 decoder implementation.  
// Reentrant:     Yes  
//  
// -----  
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//  
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// marcus.geelnard at home.se  
//
```

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p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmn.cnss/1.0.0/src/nan/sha2.c

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p4://qctp411.qualcomm.com/components/rel/wlanfw_mac_core.cnss/1.0/romexport/AR6320/hw.3/include/sha2.h

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* This file defines the cryptographic functions that need to be implemented
* for wpa_supplicant and hostapd. When TLS is not used, internal
* implementation of MD5, SHA1, and AES is used and no external libraries are
* required. When TLS is enabled (e.g., by enabling EAP-TLS or EAP-PEAP), the
* crypto library used by the TLS implementation is expected to be used for
* non-TLS needs, too, in order to save space by not implementing these
* functions twice.

```
*
* Wrapper code for using each crypto library is in its own file (crypto*.c)
* and one of these files is build and linked in to provide the functions
* defined here.
*/
p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmn.cnss/1.0.0/src/supplicant/crypto.h

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p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmn.cnss/1.0.0/include/config.h

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p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmn.cnss/1.0.0/include/suppl_auth_api.h

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p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmn.cnss/1.0.0/include/wpa.h
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p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmn.cnss/1.0.0/include/wpa_i.h
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p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmn.cnss/1.0.0/src/supplicant/sha256.h

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p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmn.cnss/1.0.0/src/supplicant/mywpa_common.h

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/*
 * AES-based functions
 *
 * - AES Key Wrap Algorithm (128-bit KEK) (RFC3394)
 * - One-Key CBC MAC (OMAC1) hash with AES-128
 * - AES-128 CTR mode encryption
 * - AES-128 EAX mode encryption/decryption
 * - AES-128 CBC
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p4://qctp411.qualcomm.com/components/rel/wlanfw_mac_core.cnss/1.0/romexport/AR6320/hw.3/include/sha1.h

/*
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/*
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*
* First, the polynomial itself and its table of feedback terms. The
* polynomial is
*  $X^{32}+X^{26}+X^{23}+X^{22}+X^{16}+X^{12}+X^{11}+X^{10}+X^8+X^7+X^5+X^4+X^2+X^1+X^0$ 
*
* Note that we take it "backwards" and put the highest-order term in
* the lowest-order bit. The  $X^{32}$  term is "implied"; the LSB is the
*  $X^{31}$  term, etc. The  $X^0$  term (usually shown as "+1") results in

```

```

* the MSB being 1
*
* Note that the usual hardware shift register implementation, which
* is what we're using (we're merely optimizing it by doing eight-bit
* chunks at a time) shifts bits into the lowest-order term. In our
* implementation, that means shifting towards the right. Why do we
* do it this way? Because the calculated CRC must be transmitted in
* order from highest-order term to lowest-order term. UARTs transmit
* characters in order from LSB to MSB. By storing the CRC this way
* we hand it to the UART in the order low-byte to high-byte; the UART
* sends each low-bit to high-bit; and the result is transmission bit
* by bit from highest- to lowest-order term without requiring any bit
* shuffling on our part. Reception works similarly
*
* The feedback terms table consists of 256, 32-bit entries. Notes
*
* The table can be generated at runtime if desired; code to do so
* is shown later. It might not be obvious, but the feedback
* terms simply represent the results of eight shift/xor opera
* tions for all combinations of data and CRC register values
*
* The values must be right-shifted by eight bits by the "updcrc
* logic; the shift must be unsigned (bring in zeroes). On some
* hardware you could probably optimize the shift in assembler by
* using byte-swap instructions
* polynomial $edb88320
*
* CRC32 code derived from work by Gary S. Brown.
*/
p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmn.cnss/1.0.0/src/nan/wlan_nan_crc.c

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// @(#)queue.h 8.5 (Berkeley) 8/20/94
// $FreeBSD: src/sys/sys/queue.h,v 1.58 2004/04/07 04:19:49 imp Exp $
// $Id: //components/rel/wlanfw_cmn.cnss/1.0/cnss_proc/wlan/fwcommon/include/queue.h#1 $
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p4://qctp411.qualcomm.com/components/rel/wlanfw_cmn.cnss/1.0.0/include/queue.h

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```

```
// $Id: //components/rel/wlanfw.cnss/1.0.1/cnss_proc/wlan/fw/target/src/wlan/proto/include/if_llc.h#3 $
// $NetBSD: if_llc.h,v 1.12 1999/11/19 20:41:19 thorpej Exp $
// $Id: //components/rel/wlanfw.cnss/1.0.1/cnss_proc/wlan/fw/target/src/wlan/proto/include/if_llc.h#3 $
//
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 *
 *   @(#)if_llc.h      8.1 (Berkeley) 6/10/93
 * $FreeBSD: src/sys/net/if_llc.h,v 1.9 2002/09/23 06:25:08 alfred Exp $
 */
```

```
p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmn.cnss/1.0.0/include/if_llc.h
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 */
p4://qctp411.qualcomm.com/components/rel/wlanfw_mac_core.cnss/1.0/patch/AR6320/v3/sha1_patch.c

/*
```

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p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmn.cnss/1.0.0/src/supplicant/mywpa_common.h

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p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmh.cnss/1.0.0/src/supplicant/mywpa_cckm.c

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// $NetBSD: if_llc.h,v 1.12 1999/11/19 20:41:19 thorpej Exp $
// $Id: //components/rel/wlanfw.cnss/1.0.1/cnss_proc/wlan/fw/target/src/wlan/proto/include/if_llc.h#3 $
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* $FreeBSD: src/sys/net/if_llc.h,v 1.9 2002/09/23 06:25:08 alfred Exp $
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p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmn.cnss/1.0.0/include/if_llc.h

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/*
* AES (Rijndael) cipher
*
* Modifications to public domain implementation:
* - support only 128-bit keys
* - cleanup
* - use C pre-processor to make it easier to change S table access
* - added option (AES_SMALL_TABLES) for reducing code size by about 8 kB at
*   cost of reduced throughput (quite small difference on Pentium 4,
*   10-25% when using -O1 or -O2 optimization)
*
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/*
* rijndael-alg-fst.c
*
* @version 3.0 (December 2000)
*
* Optimised ANSI C code for the Rijndael cipher (now AES)
*
* @author Vincent Rijmen <vincent.rijmen@esat.kuleuven.ac.be>
* @author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>
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/*

* AES-based functions
*
* - AES Key Wrap Algorithm (128-bit KEK) (RFC3394)
* - One-Key CBC MAC (OMAC1) hash with AES-128
* - AES-128 CTR mode encryption
* - AES-128 EAX mode encryption/decryption
* - AES-128 CBC
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//
// This code implements the MD5 message-digest algorithm.
// The algorithm is due to Ron Rivest. This code was
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//
// Equivalent code is available from RSA Data Security, Inc.
// This code has been tested against that, and is equivalent,
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// with every copy.
//
// To compute the message digest of a chunk of bytes, declare an
// MD5Context structure, pass it to MD5Init, call MD5Update as
// needed on buffers full of bytes, and then call MD5Final, which
// will fill a supplied 16-byte array with the digest.
//
p4://qctp411.qualcomm.com/components/rel/wlanfw_mac_core.cnss/1.0/src/misc/crypto/md5.c
```

```
/*=====
 @file secrsa_encryption.c

 This file provides RSA PKCS #1 v1.5 encryption/decryption implementation.
=====*/
```

EDIT HISTORY FOR FILE

```
$Header:
//components/dev/wlanfw.cnss/1.0.1/dennis.rome_emu_es2_rel_fw/cnss_proc/wlan/fw/target/src/misc/binisig/rsa.c#2 $
$DateTime: 2013/10/21 08:09:59 $
$Author: changhon $
```

when	who	what, where, why
04/11/11	qxu	initial version

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```
/*=====
p4://qctp411.qualcomm.com/components/rel/wlanfw_mac_core.cnss/1.0/src/misc/binisig/rsa.c
```

```
/*=====
 * $File: //components/rel/wlanfw.cnss/1.0.1/cnss_proc/wlan/fw/target/include/cil.h $
 * $Revision: #3 $
 * $Date: 2013/08/06 $
 * $Change: 2161544 $
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p4://qctp411.qualcomm.com/components/rel/wlanfw_mac_core.cnss/1.0/include/dev.h

/* =====

* \$File: //components/rel/wlanfw.cnss/1.0.1/cnss_proc/wlan/fw/target/include/pcd.h \$

* \$Revision: #3 \$
* \$Date: 2013/08/06 \$
* \$Change: 2161544 \$

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p4://qctp411.qualcomm.com/components/rel/wlanfw_mac_core.cnss/1.0/include/pcd.h

/* =====
* \$File: //components/rel/wlanfw.cnss/1.0.1/cnss_proc/wlan/fw/target/src/soc/usbhwcore/drv/snps/cil.c \$
* \$Revision: #3 \$
* \$Date: 2013/08/06 \$
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* ===== */

/** @file

*

* The Core Interface Layer provides basic services for accessing and
* managing the DWC_usb3 hardware. These services are used by both the
* Peripheral Controller Driver and the On The Go Driver.

*

* The CIL manages the memory map for the core so that the PCD and OTG drivers
* don't have to do this separately. The CIL also performs basic services
* that are not specific to either the Device or OTG modes of operation.
* These services include all functionality that requires specific
* knowledge of the CSR layout or the DMA descriptor (TRB) layout. Also
* included are services for invoking each of the commands provided by
* the DGCMD and DEPCMD registers (see the "Control and Status Registers"
* chapter of the USB3 controller databook for details).

*

* The Core Interface Layer has the following requirements:

* - Provides basic controller operations.

* - Minimal use of OS services.

* - The OS services used will be abstracted by using inline functions
* or macros.

*

*/

p4://qctp411.qualcomm.com/components/rel/wlanfw_mac_core.cnss/1.0/src/soc/usbhwcore/drv/snps/cil.c

/* =====

* \$File: //components/rel/wlanfw.cnss/1.0.1/cnss_proc/wlan/fw/target/src/soc/usbhwcore/drv/snps/pcd_intr.c \$

* \$Revision: #3 \$

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```

* ===== */
/** @file
 * This file contains the implementation of the PCD Interrupt handlers.
 *
 * The PCD handles the device interrupts. Many conditions can cause a
 * device interrupt. When an interrupt occurs, the device interrupt
 * service routine determines the cause of the interrupt and
 * dispatches handling to the appropriate routine. These interrupt
 * handling routines are described below.
 */
p4://qctp411.qualcomm.com/components/rel/wlanfw_mac_core.cnss/1.0/src/soc/usbhwcore/drv/snps/pcd_intr.c

/* =====
 * $File: //components/rel/wlanfw.cnss/1.0.1/cnss_proc/wlan/fw/target/include/os_dev.h $
 * $Revision: #3 $
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/* =====
 * $File: //components/rel/wlanfw.cnss/1.0.1/cnss_proc/wlan/fw/target/src/soc/usbhwcore/drv/snps/cil_intr.c $
 * $Revision: #3 $
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* ===== */

/** @file

*
* The Core Interface Layer provides basic services for accessing and
* managing the DWC_usb3 hardware. These services are used by both the
* Peripheral Controller Driver and the On The Go Driver.
*

* This file contains the common interrupt handling functions.

*/

p4://qctp411.qualcomm.com/components/rel/wlanfw_mac_core.cnss/1.0/src/soc/usbhwcore/drv/snps/cil_intr.c

/* =====

* \$File: //components/rel/wlanfw.cnss/1.0.1/cnss_proc/wlan/fw/target/src/soc/usbhwcore/drv/snps/pcd.c \$
* \$Revision: #3 \$
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* ===== */

/** @file

* This file implements the Peripheral Controller Driver.

*

* The Peripheral Controller Driver (PCD) is responsible for translating
* requests from the Function Driver into the appropriate actions on the
* DWC_usb3 controller. It isolates the Function Driver from the specifics
* of the controller by providing an API to the Function Driver.

*

* The Peripheral Controller Driver for Linux will implement the Gadget API,
* so that the existing Gadget drivers can be used. (Gadget Driver is the
* Linux terminology for a Function Driver.)

*

* The Linux Gadget API is defined in the header file
* <code><linux/usb/gadget.h></code>. The USB EP operations API is defined
* in the structure <code>usb_ep_ops</code> and the USB Controller API is
* defined in the structure <code>usb_gadget_ops</code>.

*

* An important function of the PCD is managing interrupts generated by the
* DWC_usb3 controller. The implementation of the DWC_usb3 device mode
* interrupt service routines is in pcd_intr.c.

*/

p4://qctp411.qualcomm.com/components/rel/wlanfw_mac_core.cnss/1.0/src/soc/usbhwcore/drv/snps/pcd.c

/* ===== */

* \$File: //components/rel/wlanfw.cnss/1.0.1/cnss_proc/wlan/fw/target/include/usb3_hw.h \$

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//
// Name:          lzuncompr.c
// Author:        Vitaly Luban
// Based on code from Marcus Geelnard
// Description:   LZ77 decoder implementation.
// Reentrant:     Yes
//
// -----
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// marcus.geelnard at home.se
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p4://qctp411.qualcomm.com/components/rel/wlanfw_mac_core.cnss/1.0/src/misc/lzuncompr/lzuncompr1.c

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// @(#)queue.h 8.5 (Berkeley) 8/20/94  
// $FreeBSD: src/sys/sys/queue.h,v 1.58 2004/04/07 04:19:49 imp Exp $  
//
```

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/*

* AES-based functions

*

* - AES Key Wrap Algorithm (128-bit KEK) (RFC3394)

* - One-Key CBC MAC (OMAC1) hash with AES-128

* - AES-128 CTR mode encryption

* - AES-128 EAX mode encryption/decryption

* - AES-128 CBC

*

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/*

* AES (Rijndael) cipher
*
* Modifications to public domain implementation:
* - support only 128-bit keys
* - cleanup
* - use C pre-processor to make it easier to change S table access
* - added option (AES_SMALL_TABLES) for reducing code size by about 8 kB at
* cost of reduced throughput (quite small difference on Pentium 4,
* 10-25% when using -O1 or -O2 optimization)
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/*

* rijndael-alg-fst.c
*
* @version 3.0 (December 2000)
*
* Optimised ANSI C code for the Rijndael cipher (now AES)
*
*/

* @author Vincent Rijmen <vincent.rijmen@esat.kuleuven.ac.be>
* @author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>
* @author Paulo Barreto <paulo.barreto@terra.com.br>

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/*
* AES-based functions
*
* - AES Key Wrap Algorithm (128-bit KEK) (RFC3394)
* - One-Key CBC MAC (OMAC1) hash with AES-128
* - AES-128 CTR mode encryption
* - AES-128 EAX mode encryption/decryption
* - AES-128 CBC
*
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 * This file defines the cryptographic functions that need to be implemented
 * for wpa_supplicant and hostapd. When TLS is not used, internal
 * implementation of MD5, SHA1, and AES is used and no external libraries are
 * required. When TLS is enabled (e.g., by enabling EAP-TLS or EAP-PEAP), the
 * crypto library used by the TLS implementation is expected to be used for
 * non-TLS needs, too, in order to save space by not implementing these
 * functions twice.
 *
 * Wrapper code for using each crypto library is in its own file (crypto*.c)
 * and one of these files is build and linked in to provide the functions
 * defined here.
 */
```

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```

```
//
//
// This code implements the MD5 message-digest algorithm.
// The algorithm is due to Ron Rivest. This code was
// written by Colin Plumb in 1993, no copyright is claimed.
// This code is in the public domain; do with it what you wish.
//
// Equivalent code is available from RSA Data Security, Inc.
// This code has been tested against that, and is equivalent,
// except that you don't need to include two pages of legalese
// with every copy.
//
// To compute the message digest of a chunk of bytes, declare an
// MD5Context structure, pass it to MD5Init, call MD5Update as
// needed on buffers full of bytes, and then call MD5Final, which
// will fill a supplied 16-byte array with the digest.
//
```

```
/*=====
@file secrsa_encryption.c

This file provides RSA PKCS #1 v1.5 encryption/decryption implementation.
=====
```

EDIT HISTORY FOR FILE

```
$DateTime: 2013/08/06 21:06:51 $
$Author: pdhavali $
```

```
when      who      what, where, why
-----  ---  -----
04/11/11  qxu      initial version
```

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```
//  
// Name:          lzuncompr.c  
// Author:       Vitaly Luban  
// Based on code from Marcus Geelnard  
// Description:  LZ77 decoder implementation.  
// Reentrant:    Yes  
//  
// -----  
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//  
// Marcus Geelnard  
// marcus.geelnard at home.se  
//
```

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// $NetBSD: if_llc.h,v 1.12 1999/11/19 20:41:19 thorpej Exp $
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*
* @(#)if_llc.h      8.1 (Berkeley) 6/10/93
* $FreeBSD: src/sys/net/if_llc.h,v 1.9 2002/09/23 06:25:08 alfred Exp $
*/

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```
src/wlan/proto/include/if_llc.h#1
```

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```
src/wlan/proto/include/if_ethersubr.h#1
```

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//
// $Id: //depot/sw/qca_main/components/wlan/qca-wifi-fw/1.0/drivers/target/src/misc/crypto/md5.c#1 $
//
// This code implements the MD5 message-digest algorithm.
// The algorithm is due to Ron Rivest. This code was
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// with every copy.
//
// To compute the message digest of a chunk of bytes, declare an
// MD5Context structure, pass it to MD5Init, call MD5Update as
// needed on buffers full of bytes, and then call MD5Final, which
// will fill a supplied 16-byte array with the digest.
//
```

```
src/misc/crypto/md5.c#1
```

```
=====
```

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```
support/lz77/lz.h#1
support/lz77/lz.c#1
```

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```
src/misc/lzuncompr/lzuncompr1.c#1
tests/serflash/flashprog/uncompr.c#1
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//
// @(#)queue.h 8.5 (Berkeley) 8/20/94
// $FreeBSD: src/sys/sys/queue.h,v 1.58 2004/04/07 04:19:49 imp Exp $
//
```

```
src/wlan/include/queue.h#1
../include/queue.h#1
```

```
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=====

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*

*

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* - One-Key CBC MAC (OMAC1) hash with AES-128

* - AES-128 CTR mode encryption

* - AES-128 EAX mode encryption/decryption

* - AES-128 CBC

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/*

* \$DateTime: 2013/07/24 11:35:54 \$
*/

/* \$OpenBSD: string.h,v 1.17 2006/01/06 18:53:04 millert Exp \$ */
/* \$NetBSD: string.h,v 1.6 1994/10/26 00:56:30 cgd Exp \$ */

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/*

* \$DateTime: 2011/01/07 17:50:19 \$

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/* \$OpenBSD: strlcpy.c,v 1.11 2006/05/05 15:27:38 millert Exp \$ */

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// $NetBSD: if_llc.h,v 1.12 1999/11/19 20:41:19 thorpej Exp $
// $Id: //depot/sw/qca_main/components/wlan/qca-wifi-fw/1.0/drivers/target/src/wlan/proto/include/if_llc.h#2 $
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*
*  @(#)if_llc.h      8.1 (Berkeley) 6/10/93
* $FreeBSD: src/sys/net/if_llc.h,v 1.9 2002/09/23 06:25:08 alfred Exp $
*/

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```
src/wlan/proto/include/if_ethersubr.h#1
```

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//  
// $Id: //depot/sw/qca_main/components/wlan/qca-wifi-fw/1.0/drivers/target/src/misc/crypto/md5.c#1 $  
//  
// This code implements the MD5 message-digest algorithm.  
// The algorithm is due to Ron Rivest. This code was  
// written by Colin Plumb in 1993, no copyright is claimed.  
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//  
// Equivalent code is available from RSA Data Security, Inc.  
// This code has been tested against that, and is equivalent,  
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//  
// To compute the message digest of a chunk of bytes, declare an  
// MD5Context structure, pass it to MD5Init, call MD5Update as  
// needed on buffers full of bytes, and then call MD5Final, which  
// will fill a supplied 16-byte array with the digest.  
//
```

```
src/misc/crypto/md5.c#1
```

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```
support/lz77/lz.h#1
support/lz77/lz.c#1
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```
src/misc/lzuncompr/lzuncompr1.c#1  
tests/serflash/flashprog/uncompr.c#1
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// @(#)queue.h 8.5 (Berkeley) 8/20/94
// $FreeBSD: src/sys/sys/queue.h,v 1.58 2004/04/07 04:19:49 imp Exp $
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```
src/wlan/include/queue.h#1
../include/queue.h#1
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// $NetBSD: if_llc.h,v 1.12 1999/11/19 20:41:19 thorpej Exp $
// $Id: //depot/sw/qca_main/components/wlan/qca-wifi-fw/1.0/drivers/target/src/wlan/proto/include/if_llc.h#2 $
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```
src/wlan/proto/include/if_ethersubr.h#1
```

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 */  
  
//  
// $Id: //depot/sw/qca_main/components/wlan/qca-wifi-fw/1.0/drivers/target/src/misc/crypto/md5.c#1 $  
//  
// This code implements the MD5 message-digest algorithm.  
// The algorithm is due to Ron Rivest. This code was  
// written by Colin Plumb in 1993, no copyright is claimed.  
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//  
// Equivalent code is available from RSA Data Security, Inc.  
// This code has been tested against that, and is equivalent,  
// except that you don't need to include two pages of legalese  
// with every copy.  
//  
// To compute the message digest of a chunk of bytes, declare an  
// MD5Context structure, pass it to MD5Init, call MD5Update as  
// needed on buffers full of bytes, and then call MD5Final, which  
// will fill a supplied 16-byte array with the digest.  
//
```

```
src/misc/crypto/md5.c#1
```

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```
support/lz77/lz.h#1
support/lz77/lz.c#1
```

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```
src/misc/lzuncompr/lzuncompr1.c#1
tests/serflash/flashprog/uncompr.c#1
```

```
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//
// @(#)queue.h 8.5 (Berkeley) 8/20/94
// $FreeBSD: src/sys/sys/queue.h,v 1.58 2004/04/07 04:19:49 imp Exp $
//
```

```
src/wlan/include/queue.h#1
../include/queue.h#1
```

```
=====
=====
```

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* ===== */
```

```
=====  
/* =====  
* FILE: sha2.h  
* AUTHOR: Aaron D. Gifford - http://www.aarongifford.com/  
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*  
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 *
 *
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 * - AES-128 CTR mode encryption
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```

```
=====  
=====  
/*  
* $DateTime: 2013/07/24 11:35:54 $  
*/  
  
/* $OpenBSD: string.h,v 1.17 2006/01/06 18:53:04 millert Exp $ */  
/* $NetBSD: string.h,v 1.6 1994/10/26 00:56:30 cgd Exp $ */
```

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*/

```
=====  
=====  
/*  
* $DateTime: 2011/01/07 17:50:19 $  
*/  
  
/* $OpenBSD: strlcpy.c,v 1.11 2006/05/05 15:27:38 millert Exp $ */  
  
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```

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//
// \$Id: //depot/sw/qca_main/components/wlan/qca-wifi-fw/1.0/drivers/target/src/wlan/proto/include/if_llc.h#2 \$
// \$NetBSD: if_llc.h,v 1.12 1999/11/19 20:41:19 thorpej Exp \$
// \$Id: //depot/sw/qca_main/components/wlan/qca-wifi-fw/1.0/drivers/target/src/wlan/proto/include/if_llc.h#2 \$
//
/*
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*
* @(#)if_llc.h 8.1 (Berkeley) 6/10/93
* $FreeBSD: src/sys/net/if_llc.h,v 1.9 2002/09/23 06:25:08 alfred Exp $
*/

```

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wlan/protocol/src/include/if_llc.h#1

```

=====
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//
//
```

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wlan/protocol/include/if_ethersubr.h#1

```
=====
=====
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//
// Marcus Geelnard
// marcus.geelnard at home.se
//
```

wlan/protocol/support/lz77/lz.h#1

wlan/protocol/support/lz77/lz.c#1

```
=====  
=====  
  
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// marcus.geelnard at home.se  
//
```

```
wlan/mac_core/tests/serflash/flashprog/uncompr.c#1
```

```
=====  
=====  
  
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//
// @(#)queue.h 8.5 (Berkeley) 8/20/94
// $FreeBSD: src/sys/sys/queue.h,v 1.58 2004/04/07 04:19:49 imp Exp $
//
```

wlan/include/queue.h#1

```
=====  
=====  
/*  
 * SHA1 hash implementation and interface functions  
 * Copyright (c) 2003-2005, Jouni Malinen <jkmaline@cc.hut.fi>  
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 */
```

wlan/mac_core/include/sha1.h
wlan/mac_core/romexport/AR900B/hw.1/include/sha1.h
wlan/mac_core/romexport/AR900B/hw.2/include/sha1.h

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```
=====  
=====  
/*  
 * FILE: sha2.h  
 * AUTHOR: Aaron D. Gifford - http://www.aarongifford.com/  
 *  
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*
* \$Id: //depot/sw/branches/qca_rome_main/perf_pwr_offload/drivers/target/include/sha2.h#1 \$
*/

wlan/mac_core/romexport/AR900B/hw.1/include/sha2.h
wlan/mac_core/romexport/AR900B/hw.2/include/sha2.h

```
=====
```

/*
* AES-based functions
*
* - AES Key Wrap Algorithm (128-bit KEK) (RFC3394)
* - One-Key CBC MAC (OMAC1) hash with AES-128
* - AES-128 CTR mode encryption
* - AES-128 EAX mode encryption/decryption
* - AES-128 CBC
*
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*/

wlan/mac_core/include/aes_wrap.h
wlan/mac_core/romexport/AR900B/hw.1/include/aes_wrap.h
wlan/mac_core/romexport/AR900B/hw.2/include/aes_wrap.h

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```
=====
```

/*

* MD5 hash implementation and interface functions
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*/

wlan/mac_core/romexport/AR900B/hw.1/include/md5.h
wlan/mac_core/romexport/AR900B/hw.2/include/md5.h

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=====

/*
* Common helper macros, etc.
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*
*/

wlan/mac_core/romexport/AR900B/hw.1/include/seccommon.h
wlan/mac_core/romexport/AR900B/hw.2/include/seccommon.h

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*/

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wlan/mac_core/include/usb.h

wlan/mac_core/romexport/AR900B/hw.1/include/usb.h

wlan/mac_core/romexport/AR900B/hw.2/include/usb.h

/* =====

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/* =====

*/

wlan/mac_core/include/cil.h

```
wlan/mac_core/romexport/AR900B/hw.1/include/cil.h
wlan/mac_core/romexport/AR900B/hw.2/include/cil.h
wlan/mac_core/include/dev.h
wlan/mac_core/romexport/AR900B/hw.1/include/dev.h
wlan/mac_core/romexport/AR900B/hw.2/include/dev.h
wlan/mac_core/include/os_dev.h
wlan/mac_core/romexport/AR900B/hw.1/include/os_dev.h
wlan/mac_core/romexport/AR900B/hw.2/include/os_dev.h
wlan/mac_core/include/usb3_hw.h
wlan/mac_core/romexport/AR900B/hw.1/include/usb3_hw.h
wlan/mac_core/romexport/AR900B/hw.2/include/usb3_hw.h
wlan/mac_core/include/pcd.h
wlan/mac_core/romexport/AR900B/hw.1/include/pcd.h
wlan/mac_core/romexport/AR900B/hw.2/include/pcd.h
```

```
=====  
=====  
/*  
 * $Header: //source/qcom/qct/core/api/kernel/main/latest/libstd/stringl/stringl.h#13 $  
 * $DateTime: 2013/07/24 11:35:54 $  
 */
```

```
/* $OpenBSD: string.h,v 1.17 2006/01/06 18:53:04 millert Exp $ */  
/* $NetBSD: string.h,v 1.6 1994/10/26 00:56:30 cgd Exp $ */
```

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 *  
 * @(#)string.h 5.10 (Berkeley) 3/9/91  
 */
```

```
wlan/protocol/support/bin2srec/include/stringl/stringl.h  
=====
```

```
=====  
/*  
* $Header: //source/qcom/qct/core/kernel/libstd/main/latest/src/strlcpy.c#1 $  
* $DateTime: 2011/01/07 17:50:19 $  
*/
```

```
/* $OpenBSD: strlcpy.c,v 1.11 2006/05/05 15:27:38 millert Exp $ */
```

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*/
```

```
wlan/protocol/support/bin2srec/strlcpy.c
```

```
=====  
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wlan/mac_core/patch/AR900B/v2/cmnos_printf_patch.c

=====

[Exhibit-174]

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By Tim Baverstock <warwick@mmm.co.uk>, Multi Media Machine Ltd.
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Stuff stolen from pam_rootok and pam_listfile

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The source for the has is derived CommonCrypto files CommonDigest.h, md32_common.h, md5_locl.h, md5_locl.h, and openssl/md5.h.

The following changes have been made to the original sources:

- replaced CC_LONG w/ mDNsu32
- replaced CC_MD5* with MD5*
- replaced CC_LONG w/ mDNsu32, removed conditional #defines from md5.h
- removed extern decls for MD5_Init/Update/Final from CommonDigest.h
- removed APPLE_COMMON_DIGEST specific #defines from md5_locl.h

Note: machine archetecure specific conditionals from the original sources are turned off, but are left in the code to aid in platform-specific optimizations and debugging.

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CommonDigest.c - APSL

md32_Common.h

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md5_dgst.c, md5_locl.h

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3. [3]Bernd Altmeier <altmeier@atlsoft.de> hopf Elektronik serial line and PCI-bus devices
4. [4]Viraj Bais <vbais@mailman1.intel.com> and [5]Clayton Kirkwood <kirkwood@striderfm.intel.com> port to WindowsNT 3.5
5. [6]Michael Barone <michael.barone@lmco.com> GPSVME fixes
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7. [8]Greg Brackley <greg.brackley@bigfoot.com> Major rework of WINNT

- port. Clean up recvbuf and iosignal code into separate modules.
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 13. [14]Steve Clift <clift@ml.csiro.au> OMEGA clock driver
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 15. [16]Sven Dietrich <sven_dietrich@trimble.com> Palisade reference clock driver, NT adj. residuals, integrated Greg's Winnt port.
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 34. [37]Lars H. Mathiesen <thorinn@diku.dk> adaptation of foundation code for Version 3 as specified in RFC-1305
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 37. [40]Wolfgang Moeller <moeller@gwdgv1.dnet.gwdg.de> VMS port
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 41. [44]Derek Mulcahy <derek@toybox.demon.co.uk> and [45]Damon Hart-Davis <d@hd.org> ARCRON MSF clock driver
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 43. [47]Rainer Pruy <Rainer.Pruy@informatik.uni-erlangen.de> monitoring/trap scripts, statistics file handling

44. [48]Dirce Richards <dirce@zk3.dec.com> Digital UNIX V4.0 port
 45. [49]Wilfredo S 兕 chez <wsanchez@apple.com> added support for NetInfo
 46. [50]Nick Sayer <mrapple@quack.kfu.com> SunOS streams modules
 47. [51]Jack Sasportas <jack@innovativeinternet.com> Saved a Lot of space on the stuff in the html/pic/ subdirectory
 48. [52]Ray Schnitzler <schnitz@unipress.com> Unixware1 port
 49. [53]Michael Shields <shields@tembel.org> USNO clock driver
 50. [54]Jeff Steinman <jss@pebbles.jpl.nasa.gov> Datum PTS clock driver
 51. [55]Harlan Stenn <harlan@pfcs.com> GNU automake/autoconfigure makeover, various other bits (see the ChangeLog)
 52. [56]Kenneth Stone <ken@sdd.hp.com> HP-UX port
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 55. [59]Paul A Vixie <vixie@vix.com> TrueTime GPS driver, generic TrueTime clock driver
 56. [60]Ulrich Windl <Ulrich.Windl@rz.uni-regensburg.de> corrected and validated HTML documents according to the HTML DTD
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*
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*
* @author Vincent Rijmen <vincent.rijmen@esat.kuleuven.ac.be>
* @author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>
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PCRE is a library of functions to support regular expressions whose syntax

and semantics are as close as possible to those of the Perl 5 language.

Written by: Philip Hazel <ph10@cam.ac.uk>

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SipHash reference C implementation

Written in 2012 by
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This is an original implementation of the DES and the crypt(3) interfaces
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An excellent reference on the underlying algorithm (and related
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B. Schneier, Applied Cryptography: protocols, algorithms,
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Changes have been made by Tony Andrews, Olaf 'Rhialto' Seibert, Robert Webb, Ciaran McCreesh and Bram Moolenaar.
Named character class support added by Walter Briscoe (1998 Jul 01)

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developed from and including some functions from:
IEC16022 bar code generation
Adrian Kennard, Andrews & Arnold Ltd
with help from Cliff Hones on the RS coding

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